

SEMESTER-V

No.	Subject Code	Name of the Subject	Periods			Evaluation Scheme				Subject Total	Credit
			L	T	P	Sessional Assessment			ESE		
						CT	TA	Total			
THEORY SUBJECTS											
1	NEC 501R	Integrated Circuits	3	1	0	30	20	50	100	150	4
2	NEC 502	Principles of Communication	3	1	0	30	20	50	100	150	4
3	NEC 503	Microprocessors	3	1	0	30	20	50	100	150	4
4	NIC 501	Control System -I	3	1	0	30	20	50	100	150	4
5	NEC 504	Antenna and Wave Propagation	2	1	0	15	10	25	50	75	3
6	NHU501	Engineering Economics	2	0	0	15	10	25	50	75	2
PRACTICAL/ DESIGN/ DRAWING											
7	NEC 551R	Integrated Circuits Lab	0	0	2	10	10	20	30	50	1
8	NIC 551	Control System Lab	0	0	2	10	10	20	30	50	1
9	NEC 552A	Communication Lab - 1	0	0	2	10	10	20	30	50	1
10	NEC 553	Microprocessors Lab	0	0	2	10	10	20	30	50	1
11	NGP 501	GP						50		50	
		TOTAL	16	5	8					1000	25

CT Class Test

AT Attendance

TA Tutorial Assignment

ESE End Semester Examination

L/T/P Lecture/ Tutorial/ Practical

SEMESTER-VI

No.	Subject Code	Name of the Subject	Periods			Evaluation Scheme				Subject Total	Credit
			L	T	P	Sessional Assessment			ESE		
						CT	TA	Total			
THEORY SUBJECTS											
1	NEC 601	Microwave Engineering	3	1	0	30	20	50	100	150	4
2	NEC 602	Digital Communication	3	1	0	30	20	50	100	150	4
3	NEC 603	Integrated Circuit Technology	3	1	0	30	20	50	100	150	4
4	NEC 0_	Departmental Elective -I	3	1	0	30	20	50	100	150	4
5	NEC 0_	Departmental Elective - II	2	1	0	15	10	25	50	75	3
6	NHU601	Industrial Management	2	0	0	15	10	25	50	75	2
PRACTICAL/ DESIGN/ DRAWING											
7	NEC 651A	Antenna and Microwave Lab	0	0	2	10	10	20	30	50	1
8	NEC 652	Communication Lab - II	0	0	2	10	10	20	30	50	1
9	NEC 653	CAD of Electronics Lab	0	0	2	10	10	20	30	50	1
10	NEC 654R	Seminar	0	0	1	10	10	20	-	20	1
11	NEC 655	Microcontrollers for Embedded Systems Lab	0	0	1	6	6	12	18	30	1
12	NGP 601	GP						50		50	
		TOTAL	16	5	8					1000	26

Departmental Elective –I

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|------------|--|
| 1. NEC011 | Digital Signal Processing |
| 2. NEC 012 | Computer Architecture and Organization |
| 3. NEC 013 | Artificial Neural Network |
| 4. NEC 014 | Advance Semiconductor Devices |
| 5. NEC013R | Real Time Systems |

Departmental Elective - II

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|-------------|--------------------------------------|
| 1. NEC 021 | Industrial Electronics |
| 2. NEC 022R | Microcontroller for Embedded Systems |
| 3. NEC 023 | Analog Signal Processing |
| 4. NEC 024R | Advance Digital Design using Verilog |

NEC 501R Integrated Circuits		
Unit	Topic	Number of Lectures
I	<p>Analog Integrated circuit Design: an overview: Current Mirrors using BJT and MOSFETs, Simple current Mirror, Base current compensated current Mirror, Wilson and Improved Wilson Current Mirrors, Widlar Current source and Cascode current Mirror</p> <p>The 741 IC Op-Amp: Bias circuit, short circuit protection circuitry, the input stage, the second stage, the output stage, and device parameters; DC Analysis of 741: Small Signal Analysis of input stage, the second stage, the output stage; Gain, Frequency Response of 741; a Simplified Model, Slew Rate, Relationship Between f_t and SR</p>	10
II	<p>Linear Applications of IC op-amps: An Overview of Op-Amp (ideal and non-ideal) based Circuits V-I and I-V converters, generalized Impedance converter, simulation of inductors</p> <p>Filters: First and second order LP, HP, BP BS and All pass active filters, KHN.</p>	8
III	<p>Digital Integrated Circuit Design-An Overview: CMOS Logic Gate Circuits: Basic Structure CMOS realization of Inverters, AND, OR, NAND and NOR Gates</p> <p>Latches and Flip flops: The Latch, The SR Flip-flop, CMOS Implementation of SR Flip- flops, A Simpler CMOS Implementation of the Clocked SR Flip-flop, D Flip-flop Circuits.</p>	8
IV	<p>Non-Linear applications of IC Op-amps: Log–Anti Log Amplifiers, Precision Rectifiers, Peak Detectors, Simple and Hold Circuits, Analog Multipliers and their applications. Op- amp as a comparator, Zero crossing detector, Schmitt Trigger, Astable multi vibrator, Mono stable multi vibrator, Generation of Triangular Waveforms</p>	7
V	<p>D/A and A/D converters</p> <p>Integrated Circuit Timer: The 555 Circuit, Implementing a Monostable Multivibrator Using the 555 IC, Astable Multi vibrator Using the 555 IC.</p> <p>Phase locked loops (PLL): Ex-OR Gates and multipliers as phase detectors, Block Diagram of IC PLL, Working of PLL and Applications of PLL.</p>	7

Text Books:

1. Sedra and Smith, “Microelectronic Circuits”, 6th Edition, Oxford University Press.
2. Michael Jacob, “Applications and Design with Analog Integrated Circuits”, PHI, 2nd Edition.

Reference Books:

1. Jacob Millman and Arvin Grabel, “Microelectronics”, 2nd Edition, Tata McGraw Hill.
2. Behzad Razavi, “Fundamentals of Microelectronics”, 2nd Edition, Wiley.
3. Mark N. Horenstein, “Microelectronic Circuits and Devices”, PHI.
4. Paul R. Gray, Paul J. Hurst, Stephen H. Lewis and Robert G. Meyer, “Analysis and Design of Analog Integrated Circuits”, Wiley.
5. Data Sheet: <http://www.ti.com/lit/ds/symlink/tl082.pdf>

6. Application Note: <http://www.ti.com/lit/an/sloa020a/sloa020a.pdf>
7. MPY634 Data Sheet: <http://www.ti.com/lit/ds/symlink/mpy634.pdf>
8. Application Note: <http://www.ti.com/lit/an/sbfa006/sbfa006.pdf>
9. ASLK Pro Manual: ASLK Manual

NEC 502 Principles of Communication		
Unit	Topic	Lectures
I	Introduction: Overview of Communications system, Communication channels, Need for modulation, Baseband and Pass band signals, Amplitude Modulation: Double side bandwidth Carrier (DSB-C), Double side band without Carrier, Single Side Band Modulation, DSB-SC, DSB-C, SSB Modulators and Demodulators, Vestigial Side Band (VSB), Quadrature Amplitude Modulator, Radio Transmitter and Receiver.	10
II	Angle Modulation, Tone Modulated FM Signal, Arbitrary Modulated FM Signal, FM Modulators and Demodulators, Approximately Compatible SSB Systems, Stereophonic FM Broadcasting, Examples Based on Mat Lab.	8
III	Pulse Modulation, Digital Transmission of Analog Signals: Sampling Theorem and its applications, Pulse Amplitude Modulation (PAM), Pulse Width Modulation, Pulse Position Modulation. Their generation and Demodulation, Digital Representation of Analog Signals, Pulse Code Modulation (PCM), PCM System, Issues in digital transmission: Frequency Division Multiplexing, Time Division Multiplexing, Line Coding and their Power Spectral density, T1 Digital System, TDM Hierarchy.	8
IV	Differential Pulse Code Modulation, Delta Modulation. Adaptive Delta Modulation, Voice Coders, Sources of Noises, Frequency domain representation of Noise, Super position of Noises, Linear filtering of Noises, Mathematical Representation of Noise.	7
V	Noise in Amplitude Modulation: Analysis, Signal to Noise Ratio, Figure of Merit. Noise in Frequency Modulation: Pre-Emphasis, De-Emphasis and SNR Improvement, Phase Locked Loops: Analog and Digital.	7

Text Book:

1. Herbert Taub and Donald L. Schilling, "Principles of Communication Systems", Tata McGraw Hill Publication.

Reference Books:

1. B.P.Lathi, "Modern Digital and Analog Communication Systems", Oxford University Press.
2. Simon Haykin, "Communication Systems", Wiley India Publication.
3. H.P.Hsu & D.Mitra, "Analog and Digital Communications", Tata McGraw-Hill Publication.

NEC 503 MICROPROCESSORS		
Unit	Topic	Lectures
1.	Evolution of microprocessors, Microprocessor architecture and its operations, 8085 pins description, programming model, basic interfacing concepts, input and output devices, logic devices and memory interfacing, addressing modes, Concept of instruction cycle, machine cycle and T-states, Concept of interrupts, Classification of 8085 instructions.	8
2.	8086 architecture-functional diagram, register organization, memory segmentation, programming model, memory address, physical memory organization, pins description, clock generator 8284A, maximum mode and minimum mode signal descriptions, timing diagrams, introduction to DOS and BIOS interrupts.	8
3.	Instruction formats, addressing modes, classification of instruction set, assembler directives (debug, TASM & MASM), macros, Programs techniques and assembly language programs: simple programs involves data transfer operation, arithmetic operation, logical operation, branch operation, machine control operation, string manipulations, stack and subroutine operations.	8
4.	8255 Programmable peripheral interfacing various mode of operation to 8086, interfacing keyboard and seven segment display, stepper motor interfacing, D/A and A/D converter, 8254 (8253) programmable interval timer, Direct Memory Access and 8237 DMA controller.	8
5.	Memory interfacing to 8086. Interrupt structure of 8086, interrupt handling, vector interrupt table and interrupt Service routine. Interfacing interrupts controller 8259 and DMA Controller 8257 to 8086. Serial communication standards, Serial data transfer schemes.	8

Text Book:

1. Ramesh Gaonkar, "Microprocessor architecture, programming and applications with the 8085", Penram International Publication (India) Pvt. Ltd.
2. Douglas V. Hall, "Microprocessors and Interfacing", Tata McGraw Hill Publication.

Reference Books:

1. Sivarama P. Dandamudi, "Introduction to Assembly Language Programming From 8086 to Pentium Processors", Springer Publication.
2. Walter A. Triebel and Avtar Singh, "The 8088 and 8086 Microprocessors: Programming, Interfacing Software, Hardware and Applications", Pearson Publication.
3. A. K. Ray and K. M. Bhurchandi, "Advance microprocessors and Peripherals" Tata McGraw Hill Publication.
4. Lyla B. Das, "The X86 Microprocessors, Architecture, Programming and Interfacing (8086 to Pentium)", Pearson Publication.

NIC 501 Control System -I		
Unit	Topic	Lectures
I	Basic Components of a control system, Feedback and its effect, types of feedback control systems. Block diagrams Reduction and signal flow graphs, Modeling of Physical systems: electrical networks, mechanical systems elements, equations of mechanical systems, sensors and encoders in control systems, DC motors in control systems.	8
II	State-Variable Analysis: Vector matrix representation of state equation, state transition matrix, state-transition equation, relationship between state equations and high-order differential equations, relationship between state equations and transfer functions. Similarity Transformation, Decomposition of transfer functions, Controllability and observability.	8
in	Time domain Analysis of Control Systems: Time response of continuous data systems, typical test signals for the time response of control systems, the unit step response and time-domain specifications, Steady-State error, time response of a first order system, transient response of a prototype second order system.	8
IV	Stability of Linear Control Systems: Bounded-input bounded-output stability continuous data systems, zero-input and asymptotic stability of continuous data systems, methods of determining stability, Routh Hurwitz criterion. Root-Locus Technique: Introduction, Properties of the Root Loci, Design aspects of the Root Loci	8
V	Frequency Domain Analysis: M_r (resonant peak) and ω_r (resonant frequency) and bandwidth of the prototype Second order system, effects of adding a zero to the forward path, effects of adding a pole to the forward path, Nyquist stability criterion, relative stability: gain margin and phase margin, stability analysis with The Bode plot.	8

Text Book:

1. B.C. Kuo & Farid Golnaraghi, "Automatic Control Systems", John Wiley India Publication.

Reference Books:

1. William A. Wolovich, "Automatic Control Systems", Oxford University Press.
2. Joseph J. Distefano III, Allen R. Stubberud, Ivan J. Williams, "Feedback and Control Systems" Schaums Outlines Series, Tata McGraw Hill Publication.
3. I. J. Nagrath & M. Gopal, "Control System Engineering", New Age International Publishers.

NEC 504 Antenna and Wave Propagation		
Unit	Topic	Lectures
I	Antennas Basics: Introduction, Basic Antenna Parameters, Patterns, Beam Area (or Beam Solid Angle) QA, Radiation Intensity, Beam Efficiency, Directivity D and Gain G, Directivity and Resolution, Antenna Apertures, Effective Height, The radio Communication link, Fields from Oscillating Dipole, Single-to-Noise Ratio(SNR), Antenna Temperature, Antenna Impedance.	8
II	Point Sources and Their Arrays: Introduction, Point Source, Power Theorem and its Application to an Isotropic Source, Radiation Intensity, Arrays of Two Isotropic Point Sources, Non-isotropic but Similar Point Sources and the Principle of Pattern Multiplication, Pattern Synthesis by Pattern Multiplication, Linear Arrays of n Isotropic Point Sources of Equal Amplitude and Spacing, Linear Broadside Arrays with Non- uniform Amplitude Distributions. General Considerations. Electric Dipoles, Thin Liner Antennas and Arrays of Dipoles and Apertures: The Short Electric Dipole, The Fields of a Short Dipole, Radiation Resistance of Short Electric Dipole, Thin Linear Antenna, Radiation Resistance of $\lambda/2$ Antenna, Array of Two Driven $\lambda/2$ Elements: Broadside Case and End-Fire Case, Horizontal Antennas Above a Plane Ground, Vertical Antennas Above a Plane Ground, Yagi-Uda Antenna Design, Long-Wire Antennas, folded Dipole Antennas.	8
III	The Loop Antenna: Design and its Characteristic Properties, Application of Loop Antennas, Far Field Patterns of Circular Loop Antennas with Uniform Current, Slot Antennas, Horn Antennas, Helical Antennas, The Log-Periodic Antenna, Micro strip Antennas. Reflector Antennas: Flat Sheet Reflectors, Corner Reflectors, The Parabola-General Properties, A Comparison Between Parabolic and Corner Reflectors, The Paraboloidal Reflector, Patterns of Large Circular Apertures with Uniform Illumination, Reflector Types (summarized), Feed Methods for Parabolic Reflectors.	8
IV	Ground Wave Propagation: Plane Earth Reflection, Space Wave and Surface Wave. Space Wave Propagation: Introduction, Field Strength Relation, Effects of Imperfect Earth, Effects of Curvature of Earth. Sky wave Propagation: Introduction structural Details of the ionosphere, Wave Propagation Mechanism, Refraction and Reflection of Sky Waves by ionosphere, Ray Path, Critical Frequency, MUF, LUF, OF, Virtual Height and Skip Distance, Relation Between MUF and the Skip Distance, Multi-Hop Propagation, Wave Characteristics.	8

Text Book:

1. John D Krauss, Ronald J Marhefka and Ahmad S. Khan, "Antennas and Wave Propagation", Tata McGraw Hill Publication.

Reference Books:

1. A. R. Harish, M. Sachidananda, "Antennas and Wave Propagation", Oxford University Press.
2. Edward Conrad Jordan and Keith George Balmain, "Electromagnetic Waves and Radiating Systems", PHI Publication.
3. A. Das, Sisir K. Das, "Microwave Engineering", Tata McGraw Hill Publication.

LABORATORY

NEC 551R INTEGRATED CIRCUITS LAB

Objective: - To design and implement the circuits to gain knowledge on performance of the circuit and its application. These circuits should also be simulated on Pspice and implemented using TL082, LM741, NE555, ASLK, MPY634 KP connecting wires, Power Supply, function generator and oscilloscope.

1. Design and test a function generator that can generate square wave and triangular wave output for a given frequency and cascade a multiplier MPY634KP in feedback loop to form VCO
2. Voltage to current and current to voltage converters.
3. Second order filters using operational amplifier in universal active filter topology for –
 - a. Low pass filter of specified cutoff frequency
 - b. High pass filter of specified frequency
 - c. Band pass filter with unit gain of specified pass band
 - d. Design a notch filter to eliminate 50Hz power line frequency
4. Wien bridge oscillator using operational amplifier.
5. Astable and monostable multivibrator using IC 555.
6. Design the following amplifiers:
 - a. A unity gain amplifier
 - b. A non-inverting amplifier with a gain of 'A'
 - c. An inverting amplifier with a gain of 'A'
 - d. Log and antilog amplifiers.
 - e. Voltage comparator and zero crossing detectors.
7. Design and test a PLL to get locked to a given frequency 'f'. Measure the locking range of the system and also measure the change in phase of the output signal as input frequency is varied within the lock range.
8. Design and test the integrator for a given time constant.
9. Design and test a high-Q Band pass self-tuned filter for a given center frequency.
10. Design and test an AGC system for a given peak amplitude of sine-wave output.
11. Design and test a Low Dropout regulator using op-amps for a given voltage regulation characteristic and compare the characteristics with TPS7250IC.
12. Design of a switched mode power supply that can provide a regulated output voltage for a given input range using the TPS40200 IC

Note: All listed experiments are compulsory. In addition to it, the Institutes may include more experiments based on the expertise.

NIC 551: Control System Lab

1. Different Toolboxes in MATLAB, Introduction to Control Systems Toolbox.
2. Determine transpose, inverse values of given matrix.
3. Plot the pole-zero configuration in s-plane for the given transfer function.
4. Determine the transfer function for given closed loop system in block diagram representation.
5. Plot unit step response of given transfer function and find peak overshoot, peak time.
6. Plot unit step response and to find rise time and delay time.
7. Plot locus of given transfer function, locate closed loop poles for different values of k.
8. Plot root locus of given transfer function and to find out S_w , W_d , W_n given root & to discuss stability.
9. Plot Bode plot of given transfer function.
10. Plot Bode plot of given transfer function and find gain and phase margins
11. Plot Nyquist plot for given transfer function and to compare the relative stability
12. Plot the Nyquist plot for given transfer function and to discuss closed loop stability, gain

and phase margin.

Note:-In addition to it, Institutes may include more experiments based on the expertise.

NEC 552: Communication Lab - 1

1. To study DSB/ SSB amplitude modulation & determine its modulation factor & power in side bands.
2. To study amplitude demodulation by linear diode detector
3. To study frequency modulation and determine its modulation factor
4. To study PLL 565 as frequency demodulator.
5. To study sampling and reconstruction of Pulse Amplitude modulation system.
6. To study the Sensitivity, Selectivity, and Fidelity characteristics of super heterodyne receiver.
7. To study Pulse Amplitude Modulation
 - a. using switching method
 - b. by sample and hold circuit
8. To demodulate the obtained PAM signal by 2nd order LPF.
9. To study Pulse Width Modulation and Pulse Position Modulation.
10. To plot the radiation pattern of a Dipole, Yagi-Uda and calculate its beam width.
11. To plot the radiation pattern of Horn, Parabolic & helical antenna. Also calculate beam width & element current.
12. Design and implement an FM radio receiver in 88-108 MHz.

NEC 553: Microprocessors Lab

1. Write a program using 8085/ 8086 Microprocessor for Decimal, Hexadecimal addition and subtraction of two Numbers.
2. Write a program using 8085/ 8086 Microprocessor for addition and subtraction of two BCD numbers.
3. To perform multiplication and division of two 8 bit numbers using 8085/ 8086.
4. To find the largest and smallest number in an array of data using 8085/8086 instruction set.
5. To write a program to arrange an array of data in ascending and descending order using 8085/ 8086.
6. To convert given Hexadecimal number into its equivalent ASCII number and vice versa using 8085/ 8086 instruction set.
7. To write a program to initiate 8251 and to check the transmission and reception of character.
8. To interface 8253 programmable interval timer to 8085/ 8086 and verify the operation of 8253 in six different modes.
9. To interface DAC with 8085/ 8086 to demonstrate the generation of square, saw tooth and triangular wave.
10. Serial communication between two 8085/8086 through RS-232 C port.

Note:-In addition, Institutes may include two more experiments based on the expertise.

NEC 601 Microwave Engineering		
Unit	Topic	Lectures
I	Rectangular Wave Guide: Field Components, TE, TM Modes, Dominant TE ₁₀ mode, Field Distribution, Power, Attenuation. Circular Waveguides: TE, TM modes. Wave Velocities, Microstrip Transmission line (TL), Coupled TL, Strip TL, Coupled Strip Line, Coplanar TL, Microwave Cavities,	8
II	Scattering Matrix, Passive microwave devices: Microwave Hybrid Circuits, Terminations, Attenuators, Phase Shifters, Directional Couplers: Two Hole directional couplers, S Matrix of a Directional coupler, Hybrid Couplers, Microwave Propagation in ferrites, Faraday Rotation, Isolators, Circulators. Spara meter analysis of all components.	8
III	Microwave Tubes: Limitation of Conventional Active Devices at Microwave frequency, Two Cavity Klystron, Reflex Klystron, Magnetron, Traveling Wave Tube, Backward Wave Oscillators: Their Schematic, Principle of Operation, Performance Characteristic and their applications.	8
IV	Solid state amplifiers and oscillators: Microwave Bipolar Transistor, Microwave tunnel diode, Microwave Field-effect Transistor, Transferred electron devices, Avalanche Transit -time devices: IMP ATT Diode, TRAPPAT Diode.	8
V	Microwave Measurements: General setup of a microwave testbench, Slotted line carriage, VSWR Meter, microwave power measurements techniques, Crystal Detector, frequency measurement, wavelength measurements, Impedance and Refection coefficient, VSWR, Insertion and attenuation loss measurements, measurement of antenna characteristics, microwave link design.	8

Text Books:

1. Samuel Y. Liao, "Microwave Devices and Circuits", Pearson Education Publication.

Reference Books:

1. R.E Collin, "Foundation for Microwave Engineering", John Wiley India Publication
2. A. Das and S.K. Das," Microwave Engineering", Tata McGraw Hill Publication.

NEC 602 Digital Communication		
Unit	Topic	Lectures
I	Digital Data transmission, Line coding review, Pulse shaping, Scrambling, Digital receivers, Eye diagram, Digital carrier system, Method of generation and detection of coherent & non-coherent binary ASK, FSK & PSK, Differential phase shift keying, quadrature modulation techniques.(QPSK and MSK),M-ary Digital carrier Modulation.	8
II	Concept of Probability, Random variable, Statistical averages, Correlation, Sum of Random Variables, Central Limit Theorem, Random Process, Classification of Random Processes, Power spectral density, Multiple random processes.	8
III	Performance Analysis of Digital communication system: Optimum linear Detector for Binary polar signaling, General Binary Signaling, Coherent Receivers for Digital Carrier Modulations, Signal Space Analysis of Optimum Detection, Vector Decomposition of White Noise Random processes, General Expression for Error Probability of optimum receivers,	8
IV	Spread Spectrum Communications: Frequency Hopping Spread Spectrum (FHSS) systems, Direct Sequence Spread Spectrum, Code Division Multiple Access of DSSS, Multiuser Detection, OFDM Communications	8
V	Measure of Information, Source Encoding, Error Free Communication over a Noisy Channel capacity of a discrete and Continuous Memoryless channel Error Correcting codes: Hamming sphere, Hamming distance and Hamming bound, relation between minimum distance and error detecting and correcting capability, Linear block codes, encoding & syndrome decoding; Cyclic codes, encoder and decoders for systematic cycle codes; convolution codes, code tree & Trellis diagram, Viterbi and sequential decoding, burst error correction, Turbo codes.	8

Text Book:

1. B.P.Lathi, "Modern Digital and Analog Communication Systems", Oxford University Press Publication.

Reference Books:

1. H. Taub, D.L. Schilling, G. Saha, "Principles of Communications", McGraw-Hill International Publication.
2. Simon Haykin, "Communication Systems", Wiley India Publication.
3. H.P.HSU and D.Mitra, "Analog and Digital Communications", TataMcGraw-Hill Publication.

NEC 603 Integrated Circuit Technology		
Unit	Topic	Lectures
I	Introduction To IC Technology: SSI, MSI, LSI, VLSI Integrated Circuits Crystal Growth and Wafer Preparation: Electronic Grade Silicon, Czochralski Crystal Growth, Silicon Shaping, Processing Considerations. Epitaxy: Vapor -Phase Epitaxy, Molecular Beam Epitaxy, Silicon on Insulators, Epitaxial Evaluation.	8
II	Oxidation: Growth Kinetics, Thin Oxides, Oxidation Techniques and Systems, Oxides Properties. Lithography: Optical Lithography. Photo masks, Wet Chemical Etching. Dielectric and Polysilicon Film Deposition: Deposition Processes, Polysilicon, Silicon Dioxide, Silicon Nitride.	8
III	Diffusion: Diffusion of Impurities in Silicon and Silicon Dioxide, Diffusion Equations, Diffusion Profiles, Diffusion Furnace, Solid, Liquid and Gaseous Sources, Sheet Resistance and its Measurement. Ion-Implantation: Ion-Implantation Technique, Range Theory, Implantation Equipment.	8
IV	Metallization: Metallization Application, Metallization Choices, Physical Vapor Deposition, Vacuum Deposition, Sputtering Apparatus. Packaging of VLSI devices: Package Types, Packaging Design Consideration, VLSI Assembly Technologies, Package Fabrication Technologies.	8
V	VLSI Process Integration: Fundamental Considerations For IC Processing, NMOS IC Technology, CMOS IC Technology, Bipolar IC Technology, Monolithic and Hybrid Integrated Circuits, IC Fabrication	8

Text Books:

1. S. M. Sze, "VLSI Technology", McGraw Hill Publication.
2. S.K. Ghandhi, "VLSI Fabrication Principles", Willy-India Pvt. Ltd.

Reference Books:

1. J. D. Plummer, M. D. Deal and Peter B. Griffin, "Silicon VLSI Technology: Fundamentals, Practice and Modelling", Pearson Education Publication.
2. Stephen A. Campbell, "Fabrication Engineering at the Micro and Nano scale", Oxford University Press.

Laboratory

NEC 651 ANTENNA AND MICROWAVE LAB

1. Study of Reflex Klystron Characteristics.
2. Measurement of guide wavelength and frequency of the signal in a rectangular Waveguide using slotted line carriage in a Micro wave Bench.
3. Measurement of impedance of an unknown load connected at the output end of the slotted line carriage in a Micro wave Bench.
4. Determine the S-parameter of any Three port Tee.
5. Determine the S-parameter of a Magic Tee.
6. Study various parameters of Isolator.
7. Measurement of attenuation of an attenuator and isolation, insertion loss, cross coupling of a circulator.
8. Determine coupling coefficient, Insertion loss, Directivity and Isolation coefficient of anti-Multi-Hole directional coupler.
9. To study working of MIC Components like Micro strip Line, Filter, Directional Coupler, Wilkinson Power Divider, Ring resonator & coupler, antennas & amplifiers.
10. Study of waveguide horn and its radiation pattern and determination of the beam width.
11. Study radiation pattern of any two types of linear antenna.

NEC 652 COMMUNICATION LAB – II

1. To construct a triangular wave with the help of Fundamental Frequency and its Harmonic component.
2. To construct a Square wave with the help of Fundamental Frequency and its Harmonic component.
3. Study of Pulse code modulation (PCM) and its demodulation using Bread Board.
4. Study of delta modulation and demodulation and observe effect of slope overload.
5. Study of pulse data coding techniques for NRZ formats.
6. Study of Data decoding techniques for NRZ formats.
7. Study of Manchester coding and decoding.
8. Study of Amplitude shift keying modulator and demodulator.
9. Study of Frequency shift keying modulator and demodulator.
10. Study of Phase shift keying modulator and demodulator
11. Study of single bit error detection and correction using Hamming code.
12. Measuring the input impedance and Attenuation of a given Transmission Line

NEC-653 CAD OF ELECTRONICS LAB

PSPICE Experiments

1. (a) Transient Analysis of BJT inverter using step input.
(b) DC Analysis (VTC) of BJT inverter with and without parameters.
2. (a) Transient Analysis of NMOS inverter using step input.
(b) Transient Analysis of NMOS inverter using pulse input.
(c) DC Analysis (VTC) of NMOS inverter with and without parameters.
3. (a) Analysis of CMOS inverter using step input.
(b) Transient Analysis of CMOS inverter using step input with parameters.
(c) Transient Analysis of CMOS inverter using pulse input.
(d) Transient Analysis of CMOS inverter using pulse input with parameters.
(e) DC Analysis (VTC) of CMOS inverter with and without parameters.
4. Transient & DC Analysis of NOR Gate inverter.

5. Transient & DC Analysis of NAND Gate.
6. VHDL Experiments
 - a. Synthesis and simulation of Full Adder.
 - b. Synthesis and Simulation of Full Subtractor.
 - c. Synthesis and Simulation of 3 X 8 Decoder.
 - d. Synthesis and Simulation of 8 X 1 Multiplexer.
 - e. Synthesis and Simulation of 9 bit odd parity generator.
 - f. Synthesis and Simulation of Flip Flop (D, and T).

NEC 655 MICROCONTROLLERS FOR EMBEDDED SYSTEMS LAB

1. Write a program of Flashing LED connected to port 1 of the 8051 Micro Controller
2. Write a program to generate 10 kHz square wave using 8051.
3. Write a program to show the use of INT0 and INT1 of 8051.
4. Write a program for temperature & to display on intelligent LCD display.
5. Write a program to generate a Ramp waveform using DAC with micro controller.
6. Write a program to Interface GPIO ports in C using MSP430 (blinking LEDs , push buttons)
7. Write a program Interface potentiometer with GPIO.
8. Write a program of PWM based Speed Control of Motor controlled by potentiometer connected to GPIO.
9. Write a program of PWM generation using Timer on MSP430 GPIO.
10. Write a program to Interface an accelerometer.
11. Write a program using USB (Sending data back and forth across a bulk transfer-mode USB connection.)
12. Write a program for Master Slave Communication between 2 MSP430s using SPI
13. Write a program of basic Wi-Fi application – Communication between two MSP430 based sensor nodes.
14. Setting up the CC3100 as a HTTP server.
15. Review of User APIs for TI CC3100 & Initialization and Setting of IP addresses.

Electives Subjects

NEC 011 Digital Signal Processing		
Unit	Topic	Lectures
I	Realization of Digital Systems: Introduction, direct form realization of IIR systems, cascade realization of an IIR systems, parallel form realization of an IIR systems, Ladder structures: continued fraction expansion of $H(z)$, example of continued fraction, realization of a ladder structure, example of a ladder realization.	8
II	Design of Infinite Impulse Response Digital Filters: Introduction to Filters, Impulse Invariant Transformation, Bi-Linear Transformation, All- Pole Analog Filters: Butterworth and Chebyshev, Design of Digital Butterworth and Chebyshev Filters.	8
III	Finite Impulse Response Filter Design: Windowing and the Rectangular Window, Other Commonly Used Windows, Examples of Filter Designs Using Windows, The Kaiser Window.	8
IV	Discrete Fourier Transforms: Definitions, Properties of the DFT, Circular Convolution, Linear Convolution.	8
V	Fast Fourier Transform Algorithms: Introduction, Decimation -In Time (DIT) Algorithm, Computational Efficiency, Decimation in Frequency (DIF) Algorithm.	8

Text Book:

1. Johnny R. Johnson, "Digital Signal Processing", PHI Publication.

Reference Books:

1. John G Prokias, Dimitris G Manolakis, "Digital Signal Processing", Pearson Education.
2. Oppenheim & Schafer, "Digital Signal Processing" PHI Publication.
3. Sanjit K. Mitra, "Digital Signal Processing: A Computer-Based Approach", McGraw Hill Publication.
4. Monson Hayes, "Digital Signal Processing", McGraw Hill Education Publication.

NEC 012 Computer Architecture and Organization		
Unit	Topic	Lectures
I	Introduction to Design Methodology: System Design - System representation, Design Process, the gate level (revision), the register level components and PLD (revision), register level design The Processor Level: Processor level components, Processor level design.	8
II	Processor basics: CPU organization- Fundamentals, Additional features Data Representation - Basic formats, Fixed point numbers, Floating point numbers. Instruction sets - Formats, Types, Programming considerations.	8

III	Data path Design: Fixed point arithmetic - Addition and subtraction, Multiplication and Division, Floating point arithmetic, pipelining.	8
IV	Control Design: basic concepts - introduction, hardwired control, Micro programmed control -introduction, multiplier control unit, CPU control unit, Pipeline control- instruction pipelines, pipeline performance.	8
V	Memory organization: Multi level memories, Address translation, Memory allocation, Caches - Main features, Address mapping, structure vs performance, System Organization: Communication methods- basic concepts, bus control. Introduction to VHDL.	8

TextBooks:

1. John P Hayes "Computer Architecture and Organisation", McGraw Hill Publication.

Reference Books:

1. M Morris Mano, "Computer System Architecture", Pearson Publication.
2. Carl Hamacher, Zvonko Vranesic and Safwat Zaky, "Computer Organization and Embedded Systems", McGraw Hill Publication.
3. David A. Patterson and John L. Hennessy, "Computer Organization and Design: The Hardware/Software Interface", Elsevier Publication.

NEC 013 Artificial Neural Network		
Unit	Topic	Lectures
I	Introduction to ANN Features, structure and working of Biological Neural Network Trends in Computing Comparison of BNN and ANN. Basics of Artificial Neural Networks - History of neural network research, characteristics of neural networks terminology, models of neuron McCulloch - Pitts model, Perceptron, Ada line model, Basic learning laws, Topology of neural network architecture.	8
II	Back propagation networks : (BPN) Architecture of feed forward network, single layer ANN, multilayer perceptron, back propagation learning, input - hidden and output layer computation, back propagation algorithm, applications, selection of tuning parameters in BPN, Numbers of hidden nodes, learning.	8
III	Activation & Synaptic Dynamics : Introduction, Activation Dynamics models, synaptic Dynamics models, stability and convergence, recall in neural networks. Basic functional units of ANN for pattern recognition tasks: Basic feed forward, Basic feedback and basic competitive learning neural network. Pattern association, pattern classification and pattern mapping tasks.	8

IV	<p>a) Feedforward neural networks - - Linear responsibility X-OR problem and solution. - Analysis of pattern mapping networks summary of basic gradient search methods.</p> <p>b) Feedback neural networks Pattern Storage networks, stochastic networks and simulated annealing, Boltzmann machine and Boltzmann learning.</p>	8
V	<p>Competitive learning neural networks : Components of CL network pattern clustering and feature. Mapping network, ART networks, Features of ART models, character recognition using ART network.</p> <p>Applications of ANN: Pattern classification - Recognition of Olympic games symbols, Recognition of printed Characters. Neocognitron - Recognition of handwritten characters. NET Talk: to convert English text to speech. Recognition of consonant vowel (CV) segments, texture classification and segmentation.</p>	8

Text Book:

1. B. Yegnanarayana, "Artificial neural Networks", PHI Publication.

Reference Books:

1. S. Raj Sekaran , Vijayalakshmi Pari, " Neural networks, Fuzzy logic and Genetic Algorithms", PHI Publication.
2. Elaine Rich and Kevin Knight, "Artificial Intelligence", TMH Publication.

NEC-013R REAL TIME SYSTEMS		
Unit	Topic	Lectures
I	<p>Introduction to Real Time System Introduction to Real time Embedded System, need for a real-time system, different kinds (reactive, time driven, deadline driven, etc.) Embedded system Design cycle, Types of Real Time systems, Real Time Applications and features, Issues in real time computing, aspects of real-time systems (timeliness, responsiveness, concurrency, predictability, correctness, robustness, fault tolerance and safety, resource limitations, RTOS necessity), real-time requirement specifications, modelling/verifying design tools (UML, state charts, etc.).</p>	8

II	<p>Embedded Hardware for Real Time System Selection criteria for Real time system - Hardware and Software perspective, need for partitioning, criteria for partitioning (performance, criticality, development ease, robustness, fault tolerance and safety, resource limitations, etc.), System Considerations, Basic development environment-host vs target concept, CPU features, Architecture, I/O Ports, on-chip peripherals, Memory, Real time implementation considerations, bus architecture, Introduction to Interrupts, Interrupt vector table, interrupt programming, Pipeline and Parallelism concepts.</p>	10
III	<p>Embedded Hardware – On chip Peripherals and Communication protocols Role of peripherals for Real time systems, On-Chip peripherals & hardware accelerators, Peripherals [Direct Memory Access, Timers, Analog to Digital Conversion (ADC), DAC, Comparator, Pulse Width Modulation (PWM)], Need of real time Communication, Communication Requirements, Timeliness, Dependability, Design Issues, Overview of Real time communication, Real time Communication Peripherals – I2C, SPI & UART. Introduction to the CCS IDE: its features, project options and basic examples Analog-to-Digital Converter Lab: Build a data acquisition system Control Peripherals Lab: Generate and graph a PWM waveform Direct Memory Access (DMA) Lab: Use DMA to buffer ADC results.</p>	12
IV	<p>Embedded Software and RTOS Software Architecture of real time System, Introduction to RTOS, role of RTOS, foreground Back ground system, pros and cons, Real time kernel, qualities of good RTOS, Functionalities of RTOS – Task Management, I/O management, Memory management, Inter Task Communication, Tasks, Task states, Task control block, attributes of TCB, Context switching, Interrupts handling, Multiprocessing and multitasking.</p>	8
V	<p>Introduction to TI C2000: Interface with actuators such as motor control enabling real time capabilities of C2000 Program to demonstrate the Task switching Simulation on CCS IDE To demonstrate the blink led application Using Hwi (Hardware Interrupt: periodically to produce an interrupt using Timers) of TI RTOS. Programming: demonstrate the Blink led application Using a Swi (Software interrupt) of TI RTOS To introduce two time-based SYS/BIOS services – Clock and Timestamp in TI RTOS; demonstrate the Task synchronization using Semaphores using TI RTOS; demonstrate Inter Task Communication Using of Mailboxes and Queues using TI RTOS; demonstrate the Communication Protocols – I2C, SPI and USART using TI.</p>	10

Text Book:

1. Real-Time Systems by Jane W. S. Liu Prentice Hall Publication
2. Krishna .C.M “Real Time Systems” Mc-Graw Hill Publication.

3. Hamid A. Toliyat and Steven G. Campbell, “DSP based Electromechanical Motion Control” CRC Press Publication.
4. Jean J Labrosse, “Embedded System Design blocks”, CMP books Publication
5. John H Davies, “MSP430 Microcontroller Basics” Newnes Publication.

Reference Book:

1. TMS320C28x CPU and Instruction Set Reference Guide, TI Literature Publication
2. TMS320x28xx, 28xxx DSP Peripheral Reference Guide, TI Literature Publication
3. C2000 Teaching CD ROM from Texas Instruments Publication
4. Introduction to the TI-RTOS Kernel Workshop Lab Manual, by Texas Instruments Publication.

NEC 014 Advance Semiconductor Devices		
Unit	Topic	Lectures
I	Physics and Properties of Semiconductors: Introduction, Crystal Structure, Energy Bands and Energy Gap, Carrier Concentration at Thermal Equilibrium, Carrier-Transport Phenomena. Phonon, Optical, and Thermal Properties, Heterojunctions and Nanostructures, Basic Equations and Examples. <i>p-n</i> Junctions, Introduction, Depletion Region, Current-Voltage Characteristics, Junction Breakdown, Transient Behavior and Noise, Terminal Functions, Heterojunctions. Metal-Semiconductor Contacts, Metal-Insulator - Semiconductor Capacitors.	8
II	Bipolar Transistors: Static Characteristics, Microwave Characteristics, Related Device Structures, Heterojunction Bipolar Transistor. MOSFETs: Basic Device Characteristics, Nonuniform Doping and Buried-Channel Device, Device Scaling and Short-Channel Effects, MOSFET Structures, Circuit Applications, Nonvolatile Memory Devices, Single-Electron Transistor. JFETs, MESFETs, and MODFETs	8
III	Tunnel Devices: Tunnel Diode, Related Tunnel Devices, Resonant-Tunneling Diode. IMPATT Diodes: Static Characteristics, Dynamic Characteristics, Power and Efficiency, Noise Behavior, Device Design and Performance, BARITT Diode, TUNNETT Diode.	8
IV	Transferred-Electron and Real-Space-Transfer Devices Thyristors and Power Devices Photonic Devices and Sensors: Radioactive Transitions, Light-Emitting Diode (LED), Laser Physics, Laser Operating Characteristics, Specialty Lasers.	8
V	Photodetectors and Solar Cells: Photoconductor, Photodiodes, Avalanche Photodiode, Phototransistor, Charge-Coupled Device (CCD), Metal-Semiconductor-Metal Photodetector, Quantum-Well Infrared Photodetector, Solar Cell. Sensors: Thermal Sensors, Mechanical Sensors, Magnetic Sensors, Chemical Sensors.	8

Text Book:

1. S. M. Sze, Kwok K. NG, "Physics of Semiconductor Devices", Wiley Publication.

Reference Books:

1. J. P. Colinge and C. A. Colinge, "Physics of Semiconductor Devices", Kluwer Academic Publishers

NEC 021 Industrial Electronics		
Unit	Topic	Lectures
I	Power Semiconductor Devices: Power semiconductor devices their symbols and static characteristics and specifications of switches, types of power electronic circuits Operation, steady state & switch characteristics & switching limits of Power Transistor Operation and steady state characteristics of Power MOSFET and IGBT Thyristor -Operation V- I characteristics, two transistor model, methods of turn-on Operation of GTO, MCT and TRIAC.	8
II	Phase Controlled Rectifiers: Phase Angle Control, Single-phase Half-wave Controlled Rectifier (One quadrant), Single-phase Full-wave Controlled Rectifier (Two quadrant Converters), Performance Factors of Line-commutated Converters, The Performance Measures of Two-pulse Converters, Three phase Controlled Converters Inverters: Introduction Thyristor Inverter Classification, Series Inverters, Parallel Inverter, Three-phase Bridge Inverters, Three-phase Bridge Inverter with Input-circuit Commutation.	8
III	Choppers: Introduction, Principle of Chopper Operation, Control Strategies, step-up/Down Chopper, Jones Chopper. Introduction to basic Cycloconverters. Control of D.C. Drives: Introduction, Basic Machine Equations, Breaking Modes, Schemes for D.C. Motor Speed Control, Single-phase Separately Excited Drives, Braking Operation of Rectifier Controlled Separately excited Motor, Single-phase Separately Excited Drives, Power Factor Improvement, Three-phase Separately Excited Drives, D.C. Chopper Drives	8
IV	Control of A.C. Drives: Introduction, basic Principle of Operation, Squirrel-cage Rotor Design, Speed Control of Induction Motors, stator Voltage Control, Variable Frequency control, Rotor Resistance Control, Slip Power Recovery Scheme, Synchronous Motor Drives	8

Text Books:

1. M. H. Rashid, "Power Electronics", Pearson Education Publication.

Reference Books:

1. M. D. Singh & K. Khanchandani, "Power Electronics", Tata McGraw Hill Publication.
2. V.R. Moorthy, "Power Electronics: Devices, Circuits and Industrial Applications", Oxford University Press,
3. M.S. Jamil Asghar, "Power Electronics", PHI Publication.

NEC 022R Microcontrollers for Embedded Systems		
Unit	Topic	Lectures
I	Introduction , Microcontrollers and Embedded systems, Overview of the 8051, Inside the 8051, Addressing modes, assembly programming, 8051 data types and directives, Interfacing with 8051, Programming the 8051 timers.	6
II	MSP430x5x series block diagram, address space, on-chip peripherals (analog and digital), and Register sets. Instruction set, instruction formats, and various addressing modes of 16-bit microcontroller; Sample embedded system on MSP430 microcontroller. Memory Mapped Peripherals, programming System registers, I/O pin multiplexing, pull up/down registers, GPIO control. Interrupts and interrupt programming.	8
III	Watch dog timer, system clocks, Timer & Real Time Clock (RTC), PWM control, timing generation and measurements. Analog interfacing and data acquisition: ADC and Comparator in MSP430, data transfer using DMA.	10
IV	Serial communication basics, Synchronous/Asynchronous interfaces (like UART, USB, SPI, and I2C). UART protocol, I2C protocol, SPI protocol. Implementing and programming UART, I2C, SPI interface using MSP430, Interfacing external devices.	10
V	Internet of Things (IoT) overview and architecture, Overview of wireless sensor networks and design examples. Various wireless connectivity: NFC, ZigBee, Bluetooth, Bluetooth Low Energy, Wi-Fi. Adding Wi-Fi capability to the Microcontroller, Embedded Wi-Fi, User APIs for Wireless and Networking applications, Building IoT applications using CC3100 user API for connecting sensors.	6

Text Book:

1. Mazidi Ali Muhammad, Mazidi Gillispie Janice, and Mc Kinlay Rolin D “ The 8051 Microcontroller and Embedded Systems using Assembly and C”, Pearson Publication.
2. John H Davies, “MSP430 Microcontroller Basics” Newnes Publication.

Reference Book:

1. TI MSP430x5xx and MSP430x6xx Family User's Guide..

NEC 023 Analog Signal Processing		
Unit	Topic	Lectures
I	Introduction to domains and the analogue/digital trade off, Introduction to basic building blocks: null or, voltage feedback amplifier, operation transconductance amplifier, current conveyer, current feedback amplifier. Analog signal filtering: introduction to bilinear transfer functions and active realizations. First-order and second-order filter realization, filter design parameters (Q and ω_0), frequency response, effect of finite gain of op-amp, realization of Single-Amplifier Biquad and General Impedance Convertor circuit.	8
II	Ideal low-pass filter, Butterworth and Chebyshev magnitude response, pole locations, low-pass filter specifications.	8
III	Delay equalization: equalization procedures, equalization with first-order and second-order modules, strategies for equalization design. Definition of Bode sensitivity.	8
IV	Properties of Lossless ladders, the general impedance convertor (GIC), optimal design of the GIC, realization of simple ladders, Gorski-Popiel's Embedding Technique, Bruton's FDNR technique, creating negative components.	8

Text Books:

1. R. Schaumann and M.E. Valkenberg, " Design of Analog Circuits", Oxford University

NEC 024R Advanced Digital System Design using Verilog		
Unit	Topic	Lectures
I	Introduction to Verilog HDL: Verilog as HDL, Levels of Design Description, Concurrency, Simulation and Synthesis, Functional Verification, System Tasks, Programming Language Interface (PLI), Module, Simulation and Synthesis Tools. Language Constructs and Conventions: Introduction, Keywords, Identifiers, White Space Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators.	8
II	Gate Level Modeling: Introduction, AND Gate Primitive, Module Structure, Other Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Construction Resolution, Net Types, Design of Basic Circuits. Modeling at Dataflow Level: Introduction, Continuous Assignment Structure, Delays and Continuous Assignments, Assignment to Vectors, Operators.	8

III	<p>Behavioral Modeling: Introduction, Operations and Assignments, Functional Bifurcation, Initial Construct, Always Construct, Assignments with Delays, Wait construct, Multiple Always Blocks, Designs at Behavioral Level, Blocking and Non-Blocking Assignments, The case statement, Simulation Flow <i>if</i> and <i>if-else</i> constructs, Assign-De-Assign construct, Repeat construct, for loop, the Disable construct, While loop, Forever loop, Parallel Blocks, Force-Release construct, Event.</p> <p>Switch Level Modeling: Basic Transistor Switches, CMOS Switch, Bi-directional Gates, Time Delays with Switch Primitives, Instantiations with Strengths and Delays, Strength Contention with Trireg Nets, Exercises.</p>	8
IV	<p>System Tasks, Functions and Compiler Directives: Parameters, Path Delays, Module Parameters, System Tasks and Functions, File-Based Tasks and Functions, Computer Directives, Hierarchical Access, User Defined Primitives.</p> <p>Sequential Circuit Description: Sequential Models – Feedback Model, Capacitive Model, Implicit Model, Basic Memory Components, Functional Register, Static Machine Coding, Sequential Synthesis.</p>	8
V	<p>Components Test and Verification: Test Bench- Combinational Circuit Testing, Sequential Circuit Testing, Test Bench Techniques, Design Verification, Assertion Verification.</p> <p>Factoring, Decomposition, BDD, Ordered BDD, LPDD, Fault Detection and Analysis in combinational and sequential systems.</p>	8

Text Books:

1. T.R. Padmanabhan, B. Bala Tripura Sundari , Design through Verilog HDL”, Wiley Publication.
2. Zainalabdien Navabi, Verilog Digital System Design, TMH Publication.
3. Richard F. Tinder, “Engineering Digital Design”, Academic Press Publication.

Reference Books:

1. Stephen. Brown and Zvonko Vranesic,” Fundamentals of Logic Design with Verilog Design”, TMH Publication.
2. Sunggu Lee ,”Advanced Digital Logic Design using Verilog, State Machine & Synthesis for FPGA “, Cengage Learning Publication.
3. Samir Palnitkar , “Verilog HDL”, 2nd Edition, Pearson Education Publication.
4. Michael D. Ciletti , “Advanced Digital Design with Verilog HDL “, PHI Publication.
5. Parag K. Lala, “Digital System Design Using PLDs”, PHI India Ltd.

2nd Year III-SEMESTER

S. No.	Subject Code	Subject Name	L-T-P	ESE Marks	Sessional		Total	Credit
					CT	TA		
1.	ROE030 to 039/ RAS301	Science Based Open Elective/ Mathematics-III	3-1-0	70	20	10	100	4
2.	RVE301/ RAS302	Universal Human Values & Professional Ethics/ Environment & Ecology	3-0-0	70	20	10	100	3
3.	REE305	Network Analysis and Synthesis	3-0-0	70	20	10	100	3
4.	REC301	Digital Logic Design	3-0-0	70	20	10	100	3
5.	REC302	Electronic Devices and Circuits	3-1-0	70	20	10	100	4
6.	REC303	Signals & Systems	3-0-0	70	20	10	100	3
7.	REC351	Digital Logic Design Lab	0-0-2	50	30	20	100	1
8.	REC352	Electronic Devices and Circuits Lab	0-0-2	50	30	20	100	1
9.	REC353	Signals & Systems Lab	0-0-2	50	30	20	100	1
10.	REC354	Electronics Workshop & PCB Design Lab	0-0-2	50	30	20	100	1
11.	RME101*	Elements of Mechanical Engineering*	3-1-0	70	20	10	100*	--
12.	RCE151*	Computer Aided Engineering Graphics*	0-0-3	50	30	20	100*	--
Total							1000	24

CT: Class Test

TA: Teacher Assessment

L/T/P: Lecture/ Tutorial/ Practical

***B.Tech. IInd year lateral entry students belonging to B.Sc. Stream, shall clear the subjects RCE151/RCE251 and RME101/201 of the first year Engineering Programme along with the second year subjects.**

Science Based Open Electives:

- a. ROE030/ROE040 Manufacturing Process
- b. ROE031/ROE041 Introduction to soft computing
- c. ROE032/ROE042 Nano Science
- d. ROE033/ROE043 Laser System and Application
- e. ROE034/ROE044 Space Science
- f. ROE035/ROE045 Polymer Science & Technology
- g. ROE036/ROE046 Nuclear Science
- h. ROE037/ROE047 Material Science
- i. ROE038/ROE048 Discrete Mathematics
- j. ROE039/ROE049 Applied Linear Algebra

2nd Year IV-SEMESTER

S. No.	Subject Code	Subject Name	L-T-P	ESE Marks	Sessional		Total	Credit
					CT	TA		
1.	RAS401/ ROE040 to 049	Mathematics-III/ Science Based Open Elective	3-1-0	70	20	10	100	4
2.	RAS402/ RVE401	Environment & Ecology/ Universal Human Values & Professional Ethics	3-0-0	70	20	10	100	3
3.	REC401	Microprocessors & Microcontrollers	3-0-0	70	20	10	100	3
4.	REC402	Electromagnetic Field Theory	3-1-0	70	20	10	100	4
5.	REC403	Electronic Measurement & Instrumentation	3-0-0	70	20	10	100	3
6.	RCS406	Data Structure & Algorithms	3-0-0	70	20	10	100	3
7.	REC451	Microprocessors & Microcontrollers Lab	0-0-2	50	30	20	100	1
8.	REC452	Advanced Electronics System Lab	0-0-2	50	30	20	100	1
9.	REC453	Electronic Measurement & Instrumentation Lab	0-0-2	50	30	20	100	1
10.	RCS456	Data Structure & Algorithms Lab	0-0-2	50	30	20	100	1
11.	RME201*	Elements of Mechanical Engineering*	3-1-0	70	20	10	100*	--
12.	RCE251*	Computer Aided Engineering Graphics*	0-0-3	50	30	20	100*	--
Total							1000	24

CT: Class Test

TA: Teacher Assessment

L/T/P: Lecture/ Tutorial/ Practical

***B.Tech. IInd year lateral entry students belonging to B.Sc. Stream, shall clear the subjects RCE151/RCE251 and RME101/201 of the first year Engineering Programme along with the second year subjects.**

Science Based Open Electives:

- a. ROE030/ROE040 Manufacturing Process
- b. ROE031/ROE041 Introduction to soft computing
- c. ROE032/ROE042 Nano Science
- d. ROE033/ROE043 Laser System and Application
- e. ROE034/ROE044 Space Science
- f. ROE035/ROE045 Polymer Science & Technology
- g. ROE036/ROE046 Nuclear Science
- h. ROE037/ROE047 Material Science
- i. ROE038/ROE048 Discrete Mathematics
- j. ROE039/ROE049 Applied Linear Algebra

REE305: NETWORK ANALYSIS & SYNTHESIS

UNIT I

Signal Analysis, Complex Frequency, General Characteristics and Descriptions of Signals, Node Voltage Analysis, Mesh Current Analysis, Step Function and Associated Wave Forms, The Unit Impulse, Initial and final conditions, Step and Impulse Response, Response of Source Free Circuits, Forced Response, Phasor and Steady State Responses of Circuits to Sinusoidal Functions, Resonance in AC Circuits.

UNIT II

Review of Laplace Transforms, Poles and Zeroes, Initial and Final Value theorems, The transform circuit, Superposition Theorem, Thevenin's and Norton's theorems, Maximum Power Transfer Theorem, Convolution Integral, Amplitude and phase responses. Network functions.

UNIT III

Graph Theory fundamentals, Matrix Representation of Graphs, Formulation of Network Response Equations using Incidence Matrix, Duality in Networks. Computation of Ladder and Non-Ladder Networks, Routh-Hurwitz Stability Criterion, Bode Diagrams.

UNIT IV

Parameters of Two Port Networks, Correlation between Two Port Parameters, Two Port, Relation between Port Parameters, Transfer Functions using Two Port Parameters, Interconnection of TwoPorts , Reciprocal and Symmetric Networks, Terminated Two Port Networks, Interconnections of Two Port Networks, Image Impedance, Iterative Impedance. Harmonics and Dirichlet's Conditions, Waveform Symmetry and Fourier Coefficients. Filter Networks.

UNIT V

Active Network Synthesis and Realizability: Elements of Relizability Theory, Hurwitz Polynomial, Positive Real Functions (PRF), Characteristics of PRF, Methodology for Simple Network Synthesis, Synthesis of Two Element Type One Port Network.

Text Book:

1. Franklin F. Kuo, "Network Analysis and synthesis", Wiley India Pvt Ltd.
2. MS Sukhija, T.K. Nagsarkar, "Circuits and Networks", Oxford University Publication.

Reference Books:

1. ME Van Valkenberg, "Network Analysis", Prentice Hall of India Ltd.
2. Ghosh, "Network Theory: Analysis and Synthesis", PHI Learning Pvt. Ltd

REC301: DIGITAL LOGIC DESIGN

UNIT I

Digital System And Binary Numbers: Number System and its arithmetic, Signed binary numbers, Binary codes, Cyclic codes, Hamming Code, the map method up to five variable, Don't care conditions, POS simplification, NAND and NOR implementation, Quine Mc-Clusky method (Tabular method).

UNIT II

Combinational Logic: Combinational Circuits: Analysis Procedure, Design procedure, Binary adder-subtractor, Decimal adder, Binary multiplier, Magnitude comparator, Multiplexers, Demultiplexers, Decoders, Encoders.

UNIT III

Sequential Logic And Its Applications: Storage elements: latches & flip flops, Characteristic Equations of Flip Flops, Flip Flop Conversion, Shift Registers, Ripple Counters, Synchronous Counters, Other Counters: Johnson & Ring Counter.

UNIT IV

Synchronous & Asynchronous Sequential Circuits: Analysis of clocked sequential circuits with state machine designing, State reduction and assignments, Design procedure. Analysis procedure of Asynchronous sequential circuits, circuit with latches, design procedure, Reduction of state and flow table, Race-free state assignment, Hazards.

UNIT V

Memory & Programmable Logic Devices: Digital Logic Families: DTL, DCTL, TTL, ECL & CMOS etc., Fan Out, Fan in, Noise Margin; RAM, ROM, PLA, PAL; Circuits of Logic Families, Interfacing of Digital Logic Families, Circuit Implementation using ROM, PLA and PAL; CPLD and FPGA.

Text Books:

1. M. Morris Mano and M. D. Ciletti, "Digital Design", Pearson Education.
2. David J. Comer, "Digital Logic & State Machine Design", Oxford University Press.
3. RP Jain, "Modern Digital Electronics", Tata McGraw Hill Publication.

Reference Books:

1. DP Kothari and J.S. Dhillon, "Digital Circuits and Design", Pearson Education.
2. A. Anand Kumar, "Fundamentals of Digital Circuits", PHI Learning Pvt. Ltd.

REC302: ELECTRONIC DEVICES AND CIRCUITS

UNIT I

Energy Bands and Charge Carrier in Semiconductor: Bonding forces and energy bands in solids, Charge Carriers in Semiconductors, Carrier Concentrations, Drift Mechanism.

Excess carriers in Semiconductors: Optical Absorption, Carrier Lifetime: Direct Recombination, Steady State Carrier Generation, Quasi-Fermi Level, Diffusion of carriers and Einstein relation.

UNIT II

Junctions: Equilibrium Conditions, Forward and Reverse Biased Junctions; Steady State Conditions.

Optoelectronic Devices: Photodiode V-I characteristic, Photodetector, Solar Cells, Light Emitting Diode.

UNIT III

MOSFET: Device structure and its operation in equilibrium, V-I characteristics. Circuits at DC, MOSFET as Amplifier and switch, Biasing in MOS amplifier circuits, small-signal operation and models, single stage MOS amplifier, MOSFET internal capacitances and high frequency model, frequency response of CS amplifier

UNIT IV

BJT: Review of device structure operation and V-I characteristics, BJT circuits at DC, BJT as amplifier and switch, biasing in BJT amplifier circuit, small-signal operation and models, single stage BJT amplifier, BJT internal capacitances and high frequency model, frequency response of CE amplifier.

UNIT V

Feedback: The general feedback structure, properties of negative feedback, the four basic feedback topologies, the series-shunt feedback amplifier, the series-series feedback amplifier, the shunt-shunt and shunt series feedback amplifier.

Oscillators: Basic principles of sinusoidal oscillators, op-amp RC oscillator circuits, LC oscillator.

Text Book:

1. AS Sedra and K. C. Smith, "Microelectronic Circuits", Oxford University Press.
2. Millman Jacob, Christos Halkias, Satyabrata Jit, "Electronic Devices and Circuits", Tata McGraw Hill.
3. BG Streetman and S. Banerjee "Solid State Electronics Devices", Prentice Hall of India.

Reference Books:

1. Donald A. Neamen "Semiconductor Physics & Devices", Tata McGraw Hill.
2. Alok K. Dutta, "Semiconductor Devices and Circuits", Oxford University Press.
3. Jacob Millman and Arvin Grabel, "Microelectronics", Tata McGraw Hill.

REC303: SIGNALS & SYSTEMS

UNIT I

Signals: Representation of Signals, Singularity Functions, Discrete Time Signals, Types of Signals, Time Scaling and Shifting, Convolution and Correlation of LTI Systems, Correlation of energy and power signals.

UNIT II

Systems and Analysis of System: System Classification, Linearity/Time Invariance, Causal System, Characterization of LTI Systems, Unit Sample Response, Generalization of D.T. Systems, Concept of Stability, Convolution Integrals/summations, Energy and Power spectral density, Properties of Power spectral Density, Analysis of First order systems, Analysis of second order systems.

UNIT III

Fourier Transforms: Properties and Significance of CTFT, CTFT of Common Signals, Inverse CTFT; Introduction to DTFT, DTFT of Common Signals, Theorems and Properties – DTFT, Inverse DTFT; Continuous Time and Discrete Time Hilbert Transform and its Properties. Introduction of Gaussian signal and its Fourier transform.

UNIT IV

Laplace Transform and Z Transform: Laplace Transforms- Introduction, Laplace Transforms of common signals, Theorems and properties of Laplace Transforms, Concept of Region of Convergence, Inverse Laplace Transforms; Z Transforms – Introduction, Z Transforms of Common Signals, Theorems and properties of Z Transforms, Inverse Z Transforms.

UNIT V

Sampling of Time Signals: Nyquist Criterion, Sampling theorem and frequency domain representation of sampling, Sampling Techniques, Reconstruction of band limited signal from its samples, Sampling of Sinusoidal and other signals.

Text Book:

1. AV Oppenheim, A.S. Willsky and S. Hamid Nawab, 'Signals and Systems', Pearson Education.
2. TK Rawat, "Signals and Systems", Oxford University Press.

Reference Books:

1. BP Lathi, "Principals of Linear Systems and Signals", Oxford University Press.
2. P. Ramakrishna Rao, 'Signal and System', Tata McGraw Hill, New Delhi.
3. Kishore S. Trivedi, "Probability & Statistics with Reliability Queuing and Computer Science Applications", Wiley Publication.

REC351: DIGITAL LOGIC DESIGN LAB

1. Introduction to digital electronics lab- nomenclature of digital ICs, specifications, study of the data sheet, Concept of V_{cc} and ground, verification of the truth tables of logic gates using TTL ICs.
2. Implementation of the given Boolean function using logic gates in both SOP and POS forms.
3. Verification of state tables of RS, JK, T and D flip-flops using NAND & NOR gates.
4. Implementation and verification of Decoder using logic gates.
5. Implementation and verification of Encoder using logic gates.
6. Implementation of 4:1 multiplexer using logic gates.
7. Implementation of 1:4 demultiplexer using logic gates.
8. Implementation of 4-bit parallel adder using 7483 IC.
9. Design, and verify the 4-bit synchronous counter.
10. Design, and verify the 4-bit asynchronous counter.
11. Implementation of Mini Project using digital integrated circuit's and other components.

REC352: ELECTRONIC DEVICES AND CIRCUITS LAB

1. **Study of Lab Equipments and Components:** CRO, Multimeter, and Function Generator, Power supply- Active, Passive Components and Bread Board.
2. **P-N Junction diode:** Characteristics of PN Junction diode - Static and dynamic resistance measurement from graph.
3. **Applications of PN Junction diode:** Half & Full wave rectifier- Measurement of V_{rms} , V_{dc} , and ripple factor.
4. **Characteristics of Zener diode:** V-I characteristics of zener diode, Graphical measurement of forward and reverse resistance..
5. **Application of Zener diode:** Zener diode as voltage regulator. Measurement of percentage regulation by varying load resistor.
6. **Characteristic of BJT:** BJT in CE configuration- Graphical measurement of h-parameters from input and output characteristics. Measurement of A_v , A_i , R_o and R_i of CE amplifier with potential divider biasing.
7. **Measurement of Operational Amplifier Parameters:** Common Mode Gain, Differential Mode Gain, CMRR, Slew Rate.
8. **Applications of Op-amp:** Op-amp as summing amplifier, Difference amplifier, Integrator and differentiator.
9. **Field Effect Transistors:** Single stage Common source FET amplifier –plot of gain in dB Vs frequency, Measurement of, bandwidth, input impedance, maximum signal handling capacity (MSHC) of an amplifier.
10. **Oscillators:** Sinusoidal Oscillators-
 - a. Wein's bridge oscillator
 - b. phase shift oscillator.
11. Simulation of Amplifier circuits studied in the lab using any available simulation software and measurement of bandwidth and other parameters with the help of simulation software.

REC353: SIGNALS & SYSTEMS LAB

1. Introduction to MATLAB
 - a. To define and use variables and functions in MATLAB.
 - b. To define and use Vectors and Matrices in MATLAB.
 - c. To study various MATLAB arithmetic operators and mathematical functions.
 - d. To create and use m-files.
2. Basic plotting of signals
 - a. To study various MATLAB commands for creating two- and three-dimensional plots.
 - b. Write a MATLAB program to plot the following Continuous time and discrete time signals
 1. Step Function
 2. Impulse Function
 3. Exponential Function
 4. Ramp Function
 5. Sine Function
3. Time and Amplitude transformations
 - a. Write a MATLAB program to perform amplitude-scaling, time-scaling and time-shifting on a given signal.
4. Convolution of given signals
 - a. Write a MATLAB program to obtain linear convolution of the given sequences.
5. Autocorrelation and Cross-correlation
 - a. Write a MATLAB program to compute autocorrelation of a sequence $x(n)$ and verify the property.
 - b. Write a MATLAB program to compute cross-correlation of sequences $x(n)$ and $y(n)$ and verify the property.
6. Fourier Series and Gibbs Phenomenon
 - a. To calculate Fourier Series coefficients associated with Square Wave.
 - b. To Sum the first 10 terms and plot the Fourier Series as a function of time
 - c. To Sum the first 50 terms and plot the Fourier Series as a function of time
7. Calculating transforms using MATLAB
 - a. Calculate and plot Fourier Transform of a given signal
 - b. Calculate and plot Z-transform of a given signal
8. Impulse response and Step response of a given system
 - a. Write a MATLAB program to find the impulse response and step response of a system from its difference equation
 - b. Compute and plot the response of a given system to a given input
9. Pole-zero diagram and bode diagram
 - a. Write a MATLAB program to find pole-zero diagram, bode diagram of a given system from the given system function
 - b. Write a MATLAB program to find, bode diagram of a given system from the given system function
10. Frequency response of a system
 - a. Write a MATLAB program to plot magnitude and phase response of a given system
11. Checking Linearity/Non-Linearity of a system using SIMULINK
 - a. Build a system that amplifies a sine wave by a factor of two.
 - b. Test the linearity of this system using SIMULINK

References:

1. “Digital Signal Processing Using MATLAB” ,Vinay K. Ingle ,John G. Proakis, Cengage Learning
2. Mathworks Website www.mathworks.com/
3. Virtual Lab Website <http://www.vlab.co.in/>, <http://iitg.vlab.co.in/?sub=59&brch=166>

REC354: ELECTRONICS WORKSHOP & PCB DESIGN LAB

1. Study of CRO, DMM & Function Generator.
2. Study of various types of Active & Passive Components based on their ratings.
3. Winding shop: Step down transformer winding of less than 5VA.
4. Soldering shop: Fabrication of DC regulated power supply
5. Identification of various types of Printed Circuit Boards (PCB) and soldering Techniques.
6. Introduction to PCB Design software
7. PCB Lab: a. Artwork & printing of a simple PCB.
b. Etching & drilling of PCB.
8. Wiring & fitting shop: Fitting of power supply along with a meter in cabinet.

RCS406: DATA STRUCTURE & ALGORITHMS

UNIT I

Abstract Data Types, Sequences as value definitions, Data types in C, Pointers in C, Data Structures and C, Arrays in C, Array as ADT, One Dimensional Array, Implementing one Dimensional Array, Array as parameters, Two Dimensional Array, Structures in C, Implementing Structures, Unions in C, Implementation of unions, Structure Parameters, Allocation of storage and scope of variables, Recursive Definition and Processes: Factorial Function, Fibonacci Sequence, Recursion in C, efficiency of Recursion, Hashing: Hash Function, Open Hashing, Closed Hashing: Linear Probing, Quadratic Probing, Double Hashing, Rehashing, Extendible Hashing.

UNIT II

Stack, Queue And Linked List: Stack definition and examples, Primitive Operations, Example Representing Stacks in C, Push And Pop Operation Implementation, Queue as ADT, C Implementation of Queues, Insert Operation, Priority Queue, Array Implementation of Priority Queue, Inserting and Removing Nodes from a list-linked Implementation of stack, Queue and Priority Queue, Other List Structures, Circular Lists: Stack and Queue as Circular List -Primitive Operations on circular lists, Header Nodes, Doubly Linked Lists, Addition of Long Positive Integers on Circular and Doubly Linked List.

UNIT III

Trees: Binary trees: Operations on Binary Trees, Applications of Binary Trees, Binary Tree Representation, Node Representation of Binary Trees, Implicit Array Representation of Binary Tree, Binary Tree Traversal in C, Threaded Binary Tree, Representing List as Binary Tree, Finding the Kth element, Deleting an Element, Trees and their applications: C Representation of trees, Tree Traversals, Evaluating an Expression Tree, Constructing a Tree.

UNIT IV

Sorting And Searching: General Background of Sorting: Efficiency Considerations, Notations, Efficiency of Sorting, Exchange Sorts: Bubble Sort; Quick Sort; Selection Sort; Binary Tree Sort; Heap Sort, Heap as a Priority Queue, Sorting Using a Heap, Heap Sort Procedure, Insertion Sorts: Simple Insertion, Shell Sort, Address Calculation Sort, Merge Sort, Radix Sort, Sequential Search: Indexed Sequential Search, Binary Search, Interpolation Search.

UNIT V

Graphs: Application of Graph, C Representation of Graphs, Transitive Closure, Warshall's Algorithm, Shortest Path Algorithm, Linked Representation of Graphs, Dijkstra's Algorithm, Graph Traversal, Traversal Methods for Graphs, Spanning Forests, Undirected Graph and their Traversals, Depth First Traversal, Application of Depth First Traversal, Efficiency of Depth First Traversal, Breadth First Traversal, Minimum Spanning Tree, Kruskal's Algorithm, Round Robin Algorithm.

Text Book:

1. Aaron M. Tenenbaum, Yeedidiah Langsam, Moshe J. Augenstein, "Data structures using C and C++", Pearson Education.
2. Reema Theraja, "Data Structure using C", OUP Publication.

References Books:

1. E. Balagurusamy, "Programming in ANSI C", Second Edition, Tata McGraw Hill Publication.
2. Robert L. Kruse, Bruce P. Leung Clovis L. Tondo, "Data Structures and Program Design in C", Pearson Education.
3. Lipschutz, "Data Structures With C", Tata McGraw-Hill Education.
4. TH Koreman, "Introduction to Algorithms", MIT Press.

REC401: MICROPROCESSORS & MICROCONTROLLERS

UNIT I

8085 MICROPROCESSOR: History and Evolution of Microprocessor and their Classification, Architecture of 8085 Microprocessor, Address / Data Bus multiplexing and demultiplexing. Status and Control signal generation, Instruction set of 8085 Microprocessor, Classification of instructions, addressing modes, timing diagram of the instructions.

UNIT II

Hardware Interfacing with 8085: Methods of data Transfer and Interrupts of 8085 microprocessor: Classification of interrupts, Programming using interrupts, Direct Memory Access, Serial and parallel data transfer, Interfacing of Memory Chips with 8085 Microprocessor, Interfacing of 8085 with 8155/8156 (RAM), 8355/8755 (ROM). Interfacing of Programmable Devices with 8085 Microprocessor, 8279 programmable Keyboard/Display interface, 8255A programmable Parallel interface, 8254 programmable Interval Timer, 8259A programmable Interrupt Controller, Assembly language programming.

UNIT III

16-bit low power MCU MSP430: Introduction to microcontrollers and embedded systems, Von Neumann (Princeton) and Harvard architecture, RISC and CISC machine, Introduction to MSP430: Architecture, Programming Techniques, Addressing Modes, Programming System registers and configuration I/O ports pull up/down registers concepts, Low Power aspects of MSP430: low power modes, Active vs Standby current consumption.

UNIT IV

Configuring Peripherals in MSP430: External interrupts and software interrupt, interrupt programming, Watchdog timer, Clock Tree in MSP430, Timer/ counter interrupt; Programming MSP430 timer, counter programming, Real Time Clock (RTC), PWM control, timing generation and measurements. Analog interfacing and data acquisition: ADC and Comparator in MSP430, data transfer using DMA.

UNIT V

Serial Communication Interfaces in MSP430: Basics of serial communication, mode of serial communication, RS232, serial communication issue, Serial port programming. Implementing and programming UART, I2C, SPI interface using MSP430, Interfacing external devices, external memory, keyboards, display devices, DAC/ADC, DC Motor, Stepper Motor, Servomotor, power management, Sensor Interfacing and signal conditioning. Case Study: MSP430 based embedded system application using the interface protocols for communication with external devices: “A Low-Power Battery less Wireless Temperature and Humidity Sensor with Passive Low Frequency RFID.

Text Book:

1. Ramesh Gaonkar, “Microprocessor Architecture, Programming, and Applications with the 8085”, Penram International Publication (India) Pvt. Ltd.
2. DV Hall, “Microprocessors Interfacing”, Tata McGraw Hill Publication.
3. N. Senthil Kumar, M. Saravanan, S. Jeevananthan, “Microprocessors and Microcontrollers”, Oxford University Press Publication.
4. Getting Started with the MSP430 Launchpad by Adrian Fernandez, Dung Dang, Newness publication ISBN-13: 978-0124115880

5. MSP430 microcontroller basics 1st Edition by John H. Davies (Author), Newnes Publication ISBN-13: 978-0750682763

Reference Books:

1. http://processors.wiki.ti.com/index.php/MSP430_LaunchPad_Low_Power_Mode
2. http://processors.wiki.ti.com/index.php/MSP430_16-Bit_Ultra-Low_Power_MCU_Training
3. AK Roy & KM Bhurchandi, “Advance Microprocessor and Peripherals (Architecture, Programming & Interfacing)”, Tata McGraw Hill Publication.

REC402: ELECTROMAGNETIC FIELD THEORY

UNIT I

Coordinate Systems and Transformation :

Basics of Vectors: Addition, subtraction and multiplications; Cartesian, Cylindrical, Spherical transformation.

Vector calculus: Differential length, area and volume, line surface and volume integrals, Del operator, Gradient, Divergence of a vector, Divergence theorem, Curl of a vector, Stokes's theorem, Laplacian of a scalar.

UNIT II

Electrostatic fields: Coulombs law and field intensity, Electric field due to charge distribution, Electric flux density, Gauss's Law- Maxwell's equation, Electric dipole and flux line, Energy density in electrostatic fields, Electric field in material space: Properties of materials, convection and conduction currents, conductors, polarization in dielectrics, Dielectric-constants, Continuity equation and relaxation time, boundary conditions, Electrostatic boundary value problems: Poisson's and Laplace's equations., Methods of Images.

UNIT III

Magneto statics : Magneto-static fields, Biot - Savart's Law, Ampere's circuit law, Maxwell's equation, Application of ampere's law, Magnetic flux density- Maxwell's equation, Maxwell's equation for static fields, magnetic scalar and vector potential.

UNIT IV

Magnetic forces: Materials and devices, Forces due to magnetic field, Magnetic torque and moment, a magnetic dipole. Magnetization in materials, Magnetic boundary conditions, Inductors and inductances, Magnetic energy.

UNIT V

Waves and Applications: Maxwell's equation, Faraday's Law, transformer and motional electromotive forces, Displacement current, Maxwell's equation in final form

Electromagnetic wave propagation: Wave propagation in loss dielectrics, Plane waves in lossless dielectrics Plane wave in free space. Plane waves in good conductors, Power and the pointing vector, Reflection of a plane wave in a normal incidence. Transmission Lines and Smith Chart.

Text Book:

1. MNO Sadiku, "Elements of Electromagnetic", Oxford University Press.

Reference Books:

1. WH Hayt and JA Buck, "Engineering Electromagnetic", McGraw- Hill Education.

REC403: ELECTRONIC MEASUREMENT AND INSTRUMENTATION

UNIT I

Unit, dimensions and standards: Scientific notations and metric prefixes. SI electrical units, SI temperature scales, Other unit systems, dimensions and standards.

Measurement Errors: Gross error, systematic error, absolute error and relative error, accuracy, precision, resolution and significant figures, Measurement error combination, basics of statistical analysis.

PMMC instrument, Galvanometer, DC ammeter, DC voltmeter, series ohm meter.

UNIT II

Transistor voltmeter circuits, AC electronic voltmeter, current measurement with electronic instruments, probes, Digital voltmeter systems, Digital multimeter, digital frequency meter System.

UNIT III

Voltmeter and ammeter methods, Wheatstone bridge, low resistance measurements, Low Resistance Measuring Instruments, AC bridge theory, capacitance bridges, Inductance bridges, Q meter.

UNIT IV

CRO: CRT, Wave Form Display, Time Base, Dual Trace Oscilloscope, measurement of voltage, frequency and phase by CRO, Oscilloscope probes, Delay time based Oscilloscopes, Sampling Oscilloscope, DSO, DSO applications.

UNIT V

Instrument calibration: Comparison method, digital multimeter as standard instrument, calibration instrument, Recorders: X-Y recorders, plotters Transducers.

Text Book:

1. David A. Bell, "Electronic Instrumentation and Measurements", Oxford University Press.

Reference Books:

1. Oliver and Cage, "Electronic Measurements and Instrumentation", Tata McGraw Hill Publication.
2. Alan S. Morris, "Measurement and Instrumentation Principles", Elsevier (Buterworth Heinmann).

REC451: MICROPROCESSORS AND MICROCONTROLLERS LAB

1. To study 8085 microprocessor system.
2. i) Write a program using 8085 Microprocessor for Decimal, Hexadecimal addition and subtraction of two Numbers.
ii) Write a program using 8085 Microprocessor for addition and subtraction of two BCD numbers.
iii) To perform multiplication and division of two 8 bit numbers using 8085.
3. Learn and understand how to configure MSP-EXP430G2 Launchpad digital I/O pins. Write a C program for configuration of GPIO ports for MSP430 (blinking LEDs, push buttons interface).

Exercises:

- a) Modify the delay with which the LED blinks.
- b) Modify the code to make the green LED blink.
- c) Modify the code to make the green and red LEDs blink:
 - i. Together
 - ii. Alternately
- d) Alter the code to turn the LED ON when the button is pressed and OFF when it is released.
- e). Alter the code to make the green LED stay ON for around 1 second every time the button is pressed.
- f). Alter the code to turn the red LED ON when the button is pressed and the green LED ON when the button is released.

4. Usage of Low Power Modes:

Configure the MSP-EXP430G2 Launchpad for Low Power Mode (LPM3) and measure current consumption both in active and low power modes. Use MSPEXP430FR5969 as hardware platform and measure active mode and standby mode current.

Exercises:

- a) How many Low power modes are supported by the MSP430G2553 platform?
 - b) Measure the Active and Standby Current consumption in LPM3 mode for the same application using MSP430F5529 LaunchPad
5. Learn and understand GPIO based Interrupt programming. Write a C program and associated GPIO ISR using interrupt programming technique.

Exercises:

- a) Write the code to enable a Timer interrupt for the pin P1.1.
 - b) Write the code to turn on interrupts globally
6. Implement Pulse Width Modulation to control the brightness of the on-board, green LED. This experiment will help you to learn and understand the configuration of PWM and Timer peripherals of the MSP430G2553.

Exercises:

- a) Observe the PWM waveform on a particular pin using CRO.
 - b) What is the maximum resolution of PWM circuitry in MSP430G2 Launchpad?
 - c) Change the above code to create a PWM signal of 75% duty cycle on particular PWM pin.
7. The main objective of this experiment is to control the on-board, red LED by the analog input from a potentiometer. This experiment will help you to learn and understand how to configure an ADC to interface with a potentiometer.

LIST OF ELECTIVES:

Elective – III NEC 03* Departmental Elective III

1. NEC 031 Information Theory & Coding
2. NEC 032 Digital Image Processing
3. NEC 033 Voice Over IP
4. NEC 034 Filter Design
5. NEC 035 Applied Fuzzy Electronic Systems

Elective – IV NEC 04* Departmental Elective IV

1. NEC 041 Electronic Switching
2. NEC 042 Digital System Design using VHDL
3. NEC 043 Speech Processing
4. NEC 044 Advanced Display Technologies & Systems
5. NEC 045 Satellite & RADAR systems

SYLLABUS

NEC 701 OPTICAL COMMUNICATION		3 1 0
Unit	Topics	Lectures
I	<p>Overview of optical fiber communication: The general system, Advantages of optical fiber communication. Optical spectral band.</p> <p>Optical Fiber waveguides: Introduction, Ray theory transmission</p> <p>Total internal reflection, acceptance angle, numerical aperture, skew rays.</p> <p>Electromagnetic mode theory for optical propagation: Electromagnetic waves, modes in a planar guide, phase and group velocity, phase shift with total internal reflection and the evanescent field, goos hanchen shift.</p>	10
II	<p>Cylindrical Fiber: modes, mode coupling, step index fibers Graded index fibers, Single mode Fiber: Cut-off wavelength, Mode field diameter and spot size, effective refractive index, Group delay and mode delay factor, The Gaussian approximation, equivalent step index methods.</p> <p>Signal distortion in optical fibers - Attenuation, Material Absorption, losses in silica glass fibers; Intrinsic absorption, Extrinsic absorption. Linear scattering losses; Ray light scattering, Mie scattering.</p> <p>Non linear Scattering losses: fiber bending losses;</p> <p>Dispersion, Chromatic dispersion: material dispersion, waveguide dispersion. Intermodal dispersion: Multimode step index fiber, Multimode graded index fiber.</p> <p>Overall fiber dispersion Multimode fiber, Dispersion modified single mode fibers ,Dispersion–shifted fiber, dispersion flatted fibers, nonzero-dispersion-shifted fibers (MZ-DSF),</p> <p>Polarization: Fiber birefringence, polarization mode dispersion, polarization-maintaining fibers, Non linear effects: Scattering effects, Kerr effects.</p>	10
III	<p>Optical sources - Light Emitting Diodes (LEDs): Structures, light source materials, Quantum Efficiency on LED Power Modulation of a LED,</p> <p>Laser Diodes- models and threshold conditions, laser diode rate equations, External quantum efficiency, resonant frequency, laser diode structures and radiation patterns, single mode lasers modulation of laser diodes, laser lines.</p>	6
IV	<p>Source to fiber power launching, Source Output patterns, Power coupling calculation, Power launching versus wavelength, equilibrium numerical aperture.</p> <p>Photo detectors: Physical principles of photodiodes: The PIN photo detector, Avalanche photodiodes.</p> <p>Photo detector Noise: Noise sources, signal to noise ration.</p> <p>Detector Response time: Depletion layer photocurrent, response time structure of in GaAs APDs, Temperature effect on Avalanche gain, comparison of photo detectors.</p>	6
V	<p>Optical receiver operation: Fundamental receiver operation: Digital signal transmission, error sources, front end amplifier.</p> <p>Digital receiver performance: Probability of error receiver sensitivity, The Quantum Unit.</p> <p>Eye Diagram: Eye Pattern Features, BER and Q Factor Measurement</p> <p>Coherent Detection: Fundamental concepts, Homodyne detection, heterodyne detection, IBER comparisons.</p> <p>Digital links: Point to point links, power penalties.</p>	8

Text Book:

1. John M. Senior, "Optical Fiber Communications", PEARSON, 3rd Edition, 2010.
2. Gerd Keiser, "Optical Fiber Communications", TMH, 4th Edition, 2008.

Reference Books:

1. Govind P. Agrawal, "Fiber Optic Communication Systems", John Wiley, 3rd Edition, 2004.
2. Joseph C. Plais, "Fiber Optic Communication", Pearson Education, 4th Ed, 2004.

NEC 702B DATA COMMUNICATION NETWORKS		3 1 0
Unit	Topics	Lectures
I	Communication problem and system models, components of communication systems, communication channels and their characteristics, mathematical models for communication channels, multiple access techniques, link budget analysis	8
II	Representation of deterministic and stochastic signals, random noise characterization in communication systems, signal-to-noise ratio, characterization of communication signals and systems: signal space representations, representation of analog and digitally modulated signals, spectral characteristics of modulated signals	8
III	Optimal receivers: Receivers for signals corrupted by AWGN, Error performance Analysis of receivers for memory-less modulation, optimal receivers for modulation methods with memory, OFDM, MIMO, Source Coding, Channel Coding (Hamming codes)	8
IV	Error Control, Flow Control, Sliding Window Protocols, HDLC, PPP, Local area networks: Ethernet, Fast Ethernet, Token Ring, Introduction to Gigabit Ethernet and Wireless LANs; Hubs, bridges and switches	8
V	MAC Layer Static Channel Allocation in LANs and MANs, Dynamic Channel Allocation in LANs and MANs, ALOHA, Carrier Sense Multiple Access Protocols, Collision-Free Protocols, Limited-Contention Protocols, Wavelength Division Multiple Access Protocols, Wireless LAN Protocols, IEEE Standard 802.3	8

Text Books:

1. Madhow, U., (2008), Fundamentals of Digital Communication, Cambridge University Press
2. Lathi, B. P. & Ding, Z., (2010), Modern Digital and Analog Communication Systems, Oxford University Press
3. Stallings, W., (2010), Data and Computer Communications, Pearson.
4. Andrew S. Tanenbaum, "Computer Networks" Pearson.
5. Ajit Pal, "Data Communication and Computer Networks", PHI
6. Dimitri Bertsekas, Robert G. Gallager, "Data Networks", Prentice Hall, 1992

NEC 703 VLSI DESIGN		3 1 0
Unit	Topic	Lectures
I	Introduction: A Brief History, Preview, MOS Transistors, CMOS Logic, CMOS Fabrication and Layout, Design Partitioning, Logic Design, Circuit Design, Physical Design, Design Verification, Fabrication, Packaging and Testing.	8
II	Delay: Introduction, Transient Response, RC delay model, Linear Delay	8

	Model, Logical Effort of Paths, Timing Analysis Delay Models. Power: Introduction, Dynamic Power, Static Power	
III	Energy – Delay Optimization, Low Power Architectures. Interconnect: Introduction, Interconnect Modelling, Interconnect Impact, Interconnect Engineering, Logical Effort with Wires	8
IV	Dynamic logic circuits: Introduction, basic principle of pass transistor circuits, synchronous dynamic circuit techniques, dynamic CMOS circuit techniques, domino CMOS logic. Semiconductor memories: Introduction, DRAM, SRAM, ROM, flash memory.	8
V	Low – Power CMOS Logic Circuits: Introduction, Overview of Power Consumption, Low – Power Design through voltage scaling, Estimation and Optimization of switching activity, Reduction of Switched Capacitance and Adiabatic Logic Circuits. Design for Testability: Introduction, Fault Types and Models, Controllability and Observability, Ad Hoc Testable Design Techniques, Scan Based and BIST Techniques	8

Text Book:

1. Neil H.E.Weste, David Money Harris, “CMOS VLSI Design – A circuits and Systems Perspective” Pearson, 4th Edition
2. Sung-Mo Kang & Yosuf Leblebici, “CMOS Digital Integrated Circuits: Analysis & Design”, TMH, 3rd Edition.

Reference Books:

1. D. A. Pucknell and K. Eshraghian, “Basic VLSI Design: Systems and Circuits”, PHI, 3rd Ed., 1994.
2. W.Wolf, Modern VLSI Design: System on Chip, Third Edition, Pearson, 2002.

ELECTIVES III

NEC 031 INFORMATION THEORY & CODING		3 1 0
Unit	Topic	Lectures
I	Entropy: Entropy, Joint Entropy and Conditional Entropy, Relative Entropy and Mutual Information, Relationship Between Entropy and Mutual Information, Chain Rules for Entropy, Relative Entropy, and Mutual Information, Jensen's Inequality and Its Consequences, Log Sum Inequality and Its Applications, Data-Processing Inequality, Sufficient Statistics, Fano's Inequality	8
II	Asymptotic Equipartition Property: Asymptotic Equipartition Property Theorem, Consequences of the AEP: Data Compression, High-Probability Sets and the Typical Set Data Compression: Examples of Codes, Kraft Inequality, Optimal Codes, Bounds on the Optimal Code Length, Kraft Inequality for Uniquely Decodable Codes, Huffman Codes, Some Comments on Huffman Codes, Optimality of Huffman Codes, Shannon–Fano–Elias Coding	8
III	Channel Capacity: Examples of Channel Capacity, 7.2 Symmetric Channels, Properties of Channel Capacity, Preview of the Channel Coding Theorem, Definitions, Jointly Typical Sequences, Channel Coding Theorem	8
IV	Block Codes Digital communication channel, Introduction to block codes, Single-parity-check codes, Product codes, Repetition codes, Hamming codes, Minimum distance of block codes, Soft-decision decoding, Automatic-repeat-request schemes Linear codes Definition of linear codes, Generator matrices, Standard array, Parity-check matrices, Error syndromes, Error detection and correction, Shortened and extended linear codes	8
V	Convolution codes Encoding convolutional codes, Generator matrices for convolutional codes, Generator polynomials for convolutional codes, Graphical representation of convolutional codes, Viterbi decoder	8

Text Books:

1. Joy A. Thomas, Thomas M. Cover, "Elements of information theory", Wiley-Interscience; 2 edition (July 18, 2006)
2. S. Gravano, "Introduction to Error Control Codes" OUP Oxford (24 May 2001)
3. Robert B. Ash, "Information Theory", Dover Publications (November 1, 1990)
4. Todd k Moon, "Error Correction Coding: Mathematical Methods and Algorithms " Wiley, 2005

NEC 032 DIGITAL IMAGE PROCESSING		3 1 0
Unit	Topic	Lectures
I	Introduction: Overview of Image Processing, Nature of Image Processing, Application area of image processing, Digital Image Representation, Types of images, Digital Image Processing Operations, Fundamental steps in DIP, Overview of Digital Image Systems, Physical Aspect of Image Acquisition, biological Aspect of Image Acquisition, sampling & quantization, Digital Halftone Process, Image storage and File formats.	8
II	Image Transforms: Need for image transforms, Properties of Fourier transform, Discrete cosine transform, Discrete sine transform, Hadamard transform, Haar transform, Slant transform, SVD and KL transforms, Comparison between transforms. Image Enhancement: Image Quality and Need for image enhancement, Image enhancement operations, Image enhancement in spatial domain, histogram based techniques, Spatial Filtering concepts, Image smoothing spatial filters, Image Sharpening spatial filters, Image smoothing in frequency domain filtering, Image sharpening in frequency domain, Homomorphism filtering.	8
III	Image Restoration: Introduction to degradation, Types of Image degradations, image degradation models, noise modeling, Estimation of degradation functions, Image restoration in presence of noise only, Periodic noise and band – pass and band reject filtering, difference between enhancement & restoration, Image restoration techniques.	8
IV	Image Compression: Image compression model, Compression algorithms and its types, Type of redundancy, lossless compression algorithms, Lossy compression algorithms, Image and video compression standards.	8
V	Image Segmentation: Introduction, Detection of Discontinuities, Edge Detection, Hough Transforms and Shape Detection, corner detection, Principle of thresholding, Principle of region - growing.	8

Text Books:

1. S. Sridhar, “Digital Image Processing”, OXFORD University Press, Second Edition.
2. Rafael C. Gonzalez Richard E woods Steven L. Eddins, “Digital Image”, Pearson.
3. Rafael C. Gonzalez Richard E woods Steven L. Eddins, “Digital Image Processing Using MATLAB”, Mc Graw Hill, 2nd Edition.
4. Anil K Jain, “Fundamentals of Digital Image Processing”, Pearson.

NEC 033 VOICE OVER IP		3 1 0
Unit	Topic	Lectures
I	<p>Introduction: Carrier-Grade, VoIP, VoIP Challenges, Overview of the IP Protocol Suite, The Internet Protocol, IP Version 6, IP Multicast, The Transmission Control Protocol, The User Datagram Protocol, The Stream Control Transmission Protocol, The Real-Time Transport Protocol, The RTP Control Protocol, Security and Performance Optimization</p> <p>Speech-Coding Techniques A Little about Speech, Audio, and Music, Voice Sampling, Voice Quality, Types of Speech Coders, Waveform Coders, Analysis-by-Synthesis Codecs, G.722–Wideband Audio</p>	8
II	<p>Signaling Protocols: H.323: Multimedia Conferencing over IP The H.323 Architecture, RAS Signaling, Call Signaling, Call Scenarios, H.245 Control Signaling, Conference Calls, Securing an H.323 Network.</p> <p>The Session Initiation Protocol The SIP Architecture, Overview of SIP Messaging Syntax, Examples of SIP Message Sequences, Redirect and Proxy Servers, The Session Description Protocol, Usage of SDP with SIP, SIP Extensions and Enhancements, Usage of SIP for Features and Services, Interworking</p>	8
III	<p>Distributed Gateways and the Softswitch Architecture Separation of Media and Call Control, Softswitch Architecture, Protocol Requirements for Controlling Media Gateways, Protocols for Controlling Media Gateways, MGCP, MEGACOP/H.248.1.</p>	8
IV	<p>VoIP and SS7 The SS7 Protocol Suite, SS7 Network Architecture, ISUP, Performance Requirements for SS7, SIGTRAN, Interworking SS7 and VoIP Architectures</p>	8
V	<p>Quality of Service The Need for QoS, Overview of QoS Solutions, The Resource Reservation Protocol, DiffServ, Multiprotocol Label Switching, Combining QoS Solutions</p>	8

Text Books:

1. Richard Swale, Daniel Collins, “ Carrier-Grade VoIP”, McGraw-Hill Education 3rd Edition, 2014.
2. Olivier Hersent, Jean Pierre Petit, David Gurle, “IP Telephony – Deploying Voice Over-IP Protocols”, John Wiley & Sons Ltd, 2005

NEC 034 FILTER DESIGN		3 1 0
Unit	Topic	Lectures
I	Introduction: Fundamentals, Types of filters and descriptive terminology, why we use Analog Filters, Circuit elements and scaling, Circuit simulation and modelling. Operational amplifiers: Opamp models, Opamp slew rate, Operational amplifiers with resistive feedback: Noninverting and Inverting, Analyzing Opamp circuits, Block diagrams and feedback, The Voltage follower, Addition and subtraction, Application of Opamp resistor circuits.	8
II	First order filter: Bilinear transfer functions and frequency response – Bilinear transfer function and its parts, realization of passive elements, Bode plots, Active realization, The effect of A(s), cascade design.	8
III	Second order low pass and band pass filters: Design parameters, Second order circuit, frequency response of low pass and band pass circuits, Integrators and others biquads.	8
IV	Second order filters with arbitrary transmission zeros: By using summing, By voltage feed forward, cascade design revisited. Low pass filters with maximally flat magnitude: the ideal low pass filter, Butterworth response, Butterworth pole locations, low pass filter specifications, arbitrary transmission zeros.	8
V	Low pass filter with equal ripple (Chebyshev) magnitude response: The chebyshev polynomial, The chebyshev magnitude response, Location of chebyshev poles, Comparison of maximally flat & equal-ripple responses, Chebyshev filter design Inverse chebyshev and cauer filters: Inverse chebyshev response, From specifications to pole and zero locations, Cauer magnitude response, Chebyshev rational functions, Cauer filter design.	8

Text Book:

1. Rolf. Schaumann, Haiqiao Xiao, Mac. E. Van Valkenburg, “Analog Filter Design”, 2nd Indian Edition, Oxford University Press.

Reference Books:

1. J. Michael Jacob ,”Applications and Design with Analog Integrated Circuits”, Second edition, Pearson.
2. T. Deliyannis, Yichuang Sun, J.K. Fidler, “Continuous-Time Active Filter Design”, CRC Press.

NEC 035 APPLIED FUZZY ELECTRONIC SYSTEMS		3 1 0
Unit	Topic	Lectures
I.	History of Fuzzy Logic, Fuzzy Sets, Possibility Distributions, Fuzzy Rules, Fuzzy Sets, Operations of Fuzzy Sets, Properties of Fuzzy Sets, Geometric Interpretations of Fuzzy Sets, Possibility Theory, Fuzzy Relations and their Compositions, Fuzzy Graphs, Fuzzy Numbers, Functions with Fuzzy Arguments, Arithmetic Operations of Fuzzy Numbers.	8
II.	Fuzzy Rules: Fuzzy Mapping Rule, Fuzzy Implication Rule, Fuzzy Rule Based Models for Function Approximations, Theoretical Foundation of Fuzzy Mapping Rules, Types of Fuzzy Rule Based Models: Mamdani Model, TSK Model, Standard Additive Model, Fuzzy Implications and Approximate Reasoning: Propositional Logic, First Order Predicate Calculus, Fuzzy Implications, Approximate Reasoning, Criteria and Family of Fuzzy Implications, Possibility vs. Probability, Probability of Fuzzy Event, Probabilistic Interpretations of Fuzzy Sets, Fuzzy Measure.	8
III.	Uncertainty in information; Classical Sets, Fuzzy Sets and their properties; Cardinality of Classical Relations and their properties, The α -Level Set, Cardinality of Fuzzy Relations and their properties; Composition; Tolerance and Equivalence relationship; Membership Functions; Fuzzification and Defuzzification process; Fuzzy to Crisp Conversions; Lambda cuts; Extension Principle, Crisp functions and its mapping, Fuzzy functions and its mapping; Fuzzy Numbers; Internal Analysis in Arithmetic.	8
IV.	Approximate method of Extension, Vertex Method, DSW Algorithm, and Restricted DSW Algorithm and their comparison, Classical Predicate Logic; Fuzzy Logic; Approximate Reasoning; Fuzzy Tautologies, Contradictions, Equivalence, and Logical Proof; Fuzzy Rule Based Systems, Models of Fuzzy AND, OR, and Inverter; Fuzzy Algebra; Truth Tables; Fuzzy Functions; Concept of Fuzzy Logic Circuits; Fuzzy Flip-Flop; Fuzzy Logic Circuits in Current Mode, Fuzzy Numbers.	8
V.	Fuzzy Logic in Control Engineering: Fundamental Issues in Control Engineering, Control Design Process, Semiformal Aspects of Design Process, Mamdani Architecture of Fuzzy Control, The Sugeno-Takagi Architecture. Fuzzy Logic in Hierarchical Control Architecture, Historical Overview and Reflections on Mamdani's Approach, Analysis of Fuzzy Control System via Lyapunov's Direct Method, Linguistic Approach to the analysis of Fuzzy Control System, Parameter Plane Theory of Stability, Takagi-Sugeno-Kang Model Of Stability Analysis.	8

Text Book:

1. John Yen, Reza Langari, "Fuzzy Logic: Intelligent Control and Information", Pearson Publication.
2. Ahmad M. Ibrahim, "Introduction to Applied Fuzzy Electronics", Prentice Hall Publication.
3. Ahmad M. Ibrahim, "Fuzzy Logic for Embedded Systems Applications", Newnes Publications.
4. Witold Pedrycz, Fernando Gomide, "Fuzzy Systems Engineering: Toward Human-Centric Computing", John Wiley Publications.

NEC 751 Optical Communication & Networking Lab

Part - A

1. Familiarisation of different types of cables and different commands.
 - a) Identify Cat5 cable , RJ 45 Connector , Crimping Tool , Wire Stripper
 - b) Use Wire Stripper for Cutting wire shield and Understanding of Internal Structure of Cat 5 Cable
 - c) Finding Pin No-1 on RJ 45 Connector and Inserting Wires in connector
 - d) Crimping of RJ45 connector using Crimping tool
 - e) Preparation of Straight cable (used for Dissimilar devices such as PC to Switch , PC to router) and Cross cables (used for similar devices such as PC to PC , Router to Router , Switch to Switch)
 - f) Understand different commands like ping, tracert, ifconfig, dig etc..

2. Making a subnet and configuring router
 - a) Understand the working of a router & method to access the router via console or using telnet, different types of cables used for connectivity.
 - b) Different types of show commands & their purpose.
 - c) Assignment of IP address and enabling layer 3 connectivity.
 - d) Implement sub netting

3. Configuring web and DHCP servers
 - a) Understand Internet Information Services tool and its installation.
 - b) To configure web services using IIS tool.
 - c) Configure DHCP

4. Configuring VLAN
 - a) Understand the configuration of Vlan in a switch
 - b) How to make the port of a switch as an access port & a trunk port, purpose of the Vlan in a network
 - c) Different types of show commands & their purpose.

5. To implement a simple file transfer protocol (FTP) using connection oriented and connectionless sockets.

6. To develop a concurrent file server that spawns several threads, one for each client requesting a specific file.

7. To develop a simple chatting application using (i) Connection oriented and (ii) Connectionless sockets

Part – B (Any 4 Experiments):

1. To setting up fiber optic analog link.
2. Study and measurement of losses in optical fiber.
3. Study and measurement of numerical aperture of optical fiber.
4. Study and perform time division multiplexing (digital).
5. Study of framing in time division multiplexing.
6. Study of Manchester coding and decoding.
7. Study of voice coding and codec chip.
8. Study and measure characteristics of fiber optic LED's and photo detector.

NEC 752A Electronics Circuit Design Lab.

In this practical course students will carry out a design oriented project work using various analog/digital building blocks which they have already studied in their analog electronic/ digital electronic courses such as Electronic circuits, integrated circuits and filter design. The project may include but not restricted to any of the following:

1. Universal op-amp based biquad
2. Universal OTA biquad
3. Amplitude control or stabilization applied to any sinusoidal oscillators
4. Op-amp/ OTA based function generator
5. Any application of log/antilog circuits
6. Any applications of analog multiplier/ divider
7. Any digital system design and its hardware implementation using TTL/ CMOS ICs
8. Any circuit idea (not studied in the course) using 555 Timer in conjunction with any other ICs

The above must include

1. Design the circuit.
2. Make hardware and measure various parameters.
3. Simulation in Spice of the designed circuit.
4. Comparison of measured and simulated results.

A report is to be made for evaluation.

NEC 801 Wireless & Mobile Communication		3 1 0
Unit	Topic	Lectures
I	Evolution of mobile radio communication fundamentals. General Model of Wireless Communication Link, Types of Signals, Cellular Infrastructure, Cellular System Components, Antennas for Cellular Systems, Operation of Cellular Systems, Channel Assignment, Frequency reuse, Channel Assignment strategies, Handoff Strategies Cellular Interferences, Sectorization; Wireless Channel and Radio Communication, Free Space Propagation Model, Channel Noise and Losses, Fading in Land Mobile Systems, Multipath Fading, Fading Effects on Signal and Frequency, Shadowing; Wireless Channel Modeling: AWGN Channel, Rayleigh Channel, Rician Fading Channel, Nakagami Fading Channel, Okumura and Hata Path Loss Model; Channel Modelling: Stochastic, Flat Fading, Wideband Time-Dispersive Channel Modelling.	8
II	Theory of Vocoders, Types of Vocoders; Spread Spectrum Modulation, Pseudo-Noise Codes with Properties and Code Generation Mechanisms, DSSS and FHSS Systems, Time Hopping and Hybrid Spread Systems; Multicarrier Modulation Techniques, Zero Inter Symbol Interference Communication Techniques, Detection Strategies, Diversity Combining Techniques: Selection Combining, Threshold Combining, Equal Gain Combining, Maximum Ratio Combining; Spatial Diversity and Multiplexing in MIMO Systems, Channel Estimation,	8
III	Equalization Techniques: Transversal Filters, Adaptive Equalizers, Zero Forcing Equalizers, Decision Feedback Equalizers, and related algorithms; Multiplexing and Multiple Access: FDMA, TDMA, CDMA, OFDMA, SC-FDMA, IDMA Schemes and Hybrid Method of Multiple Access Schemes, RAKE Receiver; Multiple Access for Radio Packet Systems: Pure ALOHA, Slotted ALOHA, CSMA and their versions; Packet and Pooling Reservation Based Multiple Access Schemes.	8
IV	GSM system for mobile Telecommunication, General Packet Radio Service, Edge Technology; CDMA Based Standards: IS 95 to CDMA 2000, Wireless Local Loop, IMT 2000 and UMTS, Long Term Evolution (LTE), Mobile Satellite Communication.	8
V	Introduction to Mobile Adhoc Networks, Bluetooth, Wi-Fi Standards, WiMax Standards, Li-Fi Communication, Ultra-Wideband Communication, Mobile data networks, Wireless Standards IMT 2000, Introduction to 4G and concept of NGN.	8

Text Book:

1. T.S. Rappaport, “Wireless Communication-Principles and practice”, Pearson Publications, Second Edition.
2. Upena Dalal, “Wireless Communication and Networks”, Oxford Press Publications.
3. T L Singal ,“Wireless Communications ”, McGraw Hill Publications.

Reference Books:

1. Andrea Goldsmith, “Wireless Communications”, Cambridge University Press.
2. S. Haykin & M. Moher, “Modern wireless communication”, Pearson, 2005.

NEC 802 OPTICAL NETWORK		3 1 0
Unit	Topic	Lectures
I	Introduction to Optical Network:- Optical Networks: multiplexing techniques, second generation optical networks. The optical layer, optical packet switching. Transmission Basics: wavelength, frequencies and channel spacing, wavelength standards. Non linear Effects: Effective length and area, stimulated brillouin scattering, stimulated raman scattering, Propagation in a non linear medium, self phase modulation, cross phase modulation Four wave mixing.	8
II	Components:-Couplers: Principles of operation, Conservation of energy, Isolators and circulators: Principles of operation Multiplexers and filters: Gratings, diffraction pattern, Bragg grating, Fiber gratings, Fabry-perot filters, multilayers dielectric thin – film filters, Mach-Zehnder interferometers, Arrayed waveguide grating, Acousto-optic tunable filter, High channel count multiplexer Architecture. Switching : large optical switches, Optical switch Technologies, large electronic switches wavelength converters: Optoelectronic Approach , optical grating, interferometric techniques wave mixing. Crosstalk: Intra-channel crosstalk, inter-channel crosstalk, crosstalk in Networks, Bidirectional system crosstalk reduction.	8
III	Networks- SONET/SDH: Multiplexing, SONET/SDH layers, SONET Frame structure, SONET/SDH physical layer, Elements of a SONET/SDH infrastructure. ATM: Function of ATM, Adaptation layers, Quality of service. IP: Routing and forwarding, QOS, WDM Network elements: Optical line terminals, Optical line amplifiers,. Optical add/Drop multiplexers: Architecture, reconfigurable OADMS, Optical cross connects: All optical OXC configuration.	8
IV	WDM Network Design Cost Trade-offs, Light path Topology Design, and Routing and wavelength assignment problems, Dimensioning Wavelength Routing Networks, Network Survivability, Basic Concepts, Protection in SONET/SDH, Protection in client layer, Optical Layer Protection, Different Schemes, Interworking between Layers, Access Networks, Network Architecture Overview, Enhanced HFC, FTTC, PON evolution	8
V	Optical Switching, OTDM, Synchronization, Header Processing, Buffering, Burst Switching, Deployment Considerations- SONET/SDH core Network	8

Text Books:

1. R. Ramaswami, & K. N. Sivarajan, “Optical Networks a Practical perspective”, Morgan Kaufmann Publishers, 3rd Ed.
2. U. Black, “Optical Networks: Third Generation Transport Systems”/ Pearson Educations

Reference Books:

1. Biswanath Mukherjee “Optical WDM Networks” Springer Pub 2006.

ELECTIVE IV

NEC 041 ELECTRONIC SWITCHING		3 1 0
Unit	Topic	Lectures
I	Evolution of switching systems: Introduction, Message switching, Circuits switching, Functions of a switching system, Register-translator-senders, Distribution frames, Crossbar switch, A general trucking, Electronic switching, Reed- electronic system, Digital switching systems.	8
II	Digital Switching: Switching functions, Space Division Switching, Time Division Switching, Two-Dimensional Switching, Digital Cross-Connect Systems , Digital Switching in an Analog Environment.	8
III	Telecom Engineering: Network Traffic Load and Parameters, Grade of Service and Blocking Probability, Modeling Switching Systems, Incoming Traffic and Service Time Characterization, Blocking models and Loss Estimates, Delay Systems	8
IV	Control of switching systems: Introduction, Call-processing functions, Common control, Reliability, availability and security; Stored-program control. Signalling: Introduction, Customer line signalling, Audio-frequency junctions and trunk circuits, FDM carrier systems, PCM signaling, Inter-register signalling, Common-channel signalling principles, CCITT signalling system no. 6 and 7, Digital customer line signalling.	8
V	Packet Switching: Packet Switching, Statistical Multiplexing, Routing Control (dynamic routing, virtual circuit routing and fixed-path routing), Flow Control, X.25, Frame Relay, TCP/IP ATM Cells, ATM Service Categories, ATM Switching (ATM Memory Switch, Space-Memory Switch, Memory-Space Switch, Memory-Space-Memory switch, Banyan Network Switch).	8

Text Books:

1. Thiagarajan Viswanathan & Manav Bhatnagar, "Telecommunication Switching Systems and Networks", PHI.
2. J.E. Flood, "Telecommunication Switching, Traffic and Networks", Pearson Education.
3. John C. Bellamy, "Digital Telephony", John Wiley, 3rd Ed.

NEC 042 DIGITAL SYSTEM DESIGN USING VHDL		3 1 0
Unit	Topic	Lectures
I	Introduction to VHDL, reserve words, structures, modeling, objects, data type and operators, sequential statements and processes, sequential modeling and attributes, conditional assignment, concatenation and case, array loops and assert statements, subprograms.	8
II	Digital System Design Automation– Abstraction Levels, System level design flow, RTL design flow, VHDL. RTL Design with VHDL – Basic structures of VHDL, Combinational circuits, Sequential circuits, Writing Test benches, Synthesis issues, VHDL Essential Terminologies VHDL Constructs for Structures and Hierarchy Descriptions – Basic Components, Component Instantiations, Iterative networks, Binding Alternatives, Association methods, generic Parameters, Design Configuration	8
III	Concurrent Constructs for RT level Descriptions – Concurrent Signal Assignments, Guarded signal assignment Sequential Constructs for RT level Descriptions – Process Statement, Sequential WAIT statement, VHDL Subprograms, VHDL library Structure, Packaging Utilities and Components, Sequential Statements. VHDL language Utilities - Type Declarations and Usage, VHDL Operators, Operator and Subprogram overloading, Other TYPES and TYPE – related issues, Predefined Attributes	8
IV	VHDL Signal Model – Characterizing hardware languages, Signal Assignments, Concurrent and Sequential Assignments, Multiple Concurrent Drivers Standard Resolution.	8
V	Hardware Cores and Models - Synthesis rules and styles, Memory and Queue Structures, Arithmetic Cores, Components with Separate Control and Data parts. Core Design Test and Testability - Issues Related to Design Test, Simple Test benches.	8

Text Books:

1. Z. Navabi, “VHDL-Modular Design and Synthesis of cores and Systems”, TMH – 3rd Edition.
2. R.D.M. Hunter, T. T. Johnson, “Introduction to VHDL” Spriger Publication, 2010.
3. J Bhasker , “VHDL Primer” –Pearson Education.

Reference Books:

3. C. H. Roth, “Digital System Design using VHDL”, PWS Publishing
4. Douglas Perry, “VHDL- Programming by examples”, MGH

NEC 043 SPEECH PROCESSING		3 1 0
Unit	Topic	Lectures
I	Digital models for speech signals: Mechanism of speech production & acoustic phonetics, the acoustic theory of speech production, lossless tube models, and digital models for speech signals.	6
II	Time Domain methods of speech sampling: Time dependent processing of speech, short time energy and average magnitude, short time average zero crossing rate, discrimination between speech & silence, pitch period estimation using parallel processing, short time autocorrelation function & AMDF, pitch period estimation using autocorrelation function.	10
III	Short time Fourier Analysis: Definition and properties, design of filter banks, implementation of filter bank summation method using FFT, spectrographic displays, pitch detection, analysis by synthesis phase, vocoder and channel vocoder.	8
IV	Homomorphic speech processing: Homomorphic system for convolution, complex cepstrum of speech, pitch detection using Homomorphic processing, formant estimation, Homomorphic vocoder.	6
V	Linear Predictive Coding of Speech: Basic principles of linear predictive analysis, the autocorrelation method, computation of the gain for the model, solution of LPC equations for auto correlation method, prediction error and normalized mean square error, frequency domain interpretation of mean squared prediction error relation of linear predictive analysis to lossless tube models, relation between various speech parameters, synthesis of speech from linear predictive parameters, application of LPC parameters.	10

Text / Reference Books:

1. R. L. Rabiner & R.W. Schafer, "Digital Processing of speech signals", Pearson Education.
2. B. Gold and Nelson Morgon, "Speech and audio signal processing", Wiley India Edition, 2006.

NEC 044 ADVANCED DISPLAY TECHNOLOGIES & SYSTEMS		3 1 0
Unit	Topic	Lectures
I	Properties of Light, Geometric Optics, Optical Modulation; Vision and Perception: Anatomy of Eye, Light Detection and Sensitivity, Spatial Vision and Pattern Perception, Binocular Vision and Depth Perception; Driving Displays: Direct Drive, Multiplex and Passive Matrix, Active Matrix Driving, Panel Interfaces, Graphic Controllers, Signal Processing Mechanism; Power Supply: Fundamentals, Power Supply Sequencing.	8
II	Display Glasses, Inorganic Semiconductor TFT Technology, Organic TFT Technology; Transparent Conductors, Patterning Processes: Photolithography for Thin Film LCD, Wet Etching, Dry Etching; Flexible Displays: Attributes, Technologies Compatible with Flexible Substrate and Applications, TFT Signal Processing Techniques; Touch Screen Technologies: Introduction, Coatings, Adhesive, Interfaces with Computer Mechanism.	8
III	Inorganic Phosphors, Cathode Ray Tubes, Vacuum Florescent Displays, Filed Emission Displays; Plasma Display Panels, LED Display Panels; Inorganic Electroluminescent Displays: Thin Film Electroluminescent Displays, AC Powder Electroluminescent Displays; Organic Electroluminescent Displays: OLEDs, Active Matrix for OLED Displays; Liquid Crystal Displays: Fundamentals and Materials, Properties of Liquid Crystals, Optics and Modeling of Liquid Crystals; LCD Device Technology: Twisted Numeric and Super twisted Numeric Displays, Smectic LCD Modes, In-Plane Switching Technology, Vertical Aligned Nematic LCD Technology, Bistable LCDs, Cholesteric Reflective Displays; LCD Addressing, LCD Backlight and Films, LCD Production, Flexoelectro-Optic LCDs.	8
IV	Paper like and Low Power Displays: Colorant Transposition Displays, MEMs Based Displays, 3-D Displays, 3-D Cinema Technology, Autostereoscopic 3-D Technology, Volumetric and 3-D Volumetric Display Technology, Holographic 3-D Technology; Mobile Displays: Trans-reflective Displays for Mobile Devices, Liquid Crystal Optics for Mobile Displays, Energy Aspects of Mobile Display Technology.	8
V	Microdisplay Technologies: Liquid Crystals on Silicon Reflective Microdisplay, Transmissive Liquid Crystal Microdisplay, MEMs Microdisplay, DLP Projection Technology; Microdisplay Applications: Projection Systems, Head Worn Displays; Electronic View Finders, Multifocas Displays, Occlusion Displays, Cognitive Engineering and Information Displays; Display Metrology, Standard Measurement Procedures, Advanced Measurement Procedures: Spatial Effects, Temporal Effects, Viewing Angle, Ambient Light; Display Technology Dependent Issues, Standards and Patterns, Green Technologies in Display Engineering.	8

Text Book:

1. Janglin Chen, Wayne Cranton, Mark Fihn , “Handbook of Visual Display Technology”, Springer Publication.

NEC 045 SATELLITE & RADAR SYSTEMS		3 1 0
Unit	Topic	Lectures
I	Introduction to radar, radar block diagram and operation, radar frequencies, Applications of radar. The Radar Equation: Detection of signals in noise , Receiver noise and the signal to noise ratio, Probabilities of detection and false alarm, Integration of Radar Pulses, Radar cross section of targets, Radar cross section fluctuations, Transmitter Power, Pulse Reception Frequency , Antenna Parameters, System Losses.	8
II	MTI and Pulse Doppler Radar: Introduction to Doppler and MTI Radar, Delay Line cancellers, Staggered Pulse Reception Frequencies, Doppler Filter Banks, Digital MTI Processing, Moving Target Detector, Limitations to MTI Performance.	8
III	Tracking Radar: sequential lobing, conical scan, monopulse Tracking, low angle tracking, tracking in range. Elements of Satellite Communications, Orbital mechanics, look angle and orbit determination, launches and launch vehicle, orbital effects. Introduction to geo-synchronous and geo-stationary satellites.	8
IV	Satellite sub-systems: Attitude and Orbit control systems, Telemetry, Tracking and command control system, Power supply system, Introduction to satellite link design, basic transmission theory, system noise temperature and G/T ratio, design of down link and uplink, design of satellite links for specified C/N, satellite data communication protocols.	8
V	Direct broadcast satellite television and radio, satellite navigation and the global positioning systems, GPS position location principle, GPS receivers and codes, Satellite Signal Acquisition, GPS navigation Message, GPS Signal Levels, Timing Accuracy, GPS Receiver Operation.	8

Text / Reference Books:

1. Merrill I. Skolnik “ Introduction to Radar Systems”, Mc Graw- Hill.
2. J.C.Toomay, Paul J. Hannen “Principles of Radar”, PHI Learning.
3. B.Pratt, A.Bostian, “Satellite Communications”, Wiley India.
4. D.Roddy, ”Satellite Communications”, TMH.